# N-1000-III/IV Installation Manual <br> Version 8.01 



## Notices

Fire Sa fety Notic e: Never connect any card reader devices or locks to door, gates or barriers without first consulting the local fire codes. You must consult with and get approval of, local fire officials before installing locks or devices on any doors that may be fire exits. Use of egress push buttons may not be legal. Single action exit may be required. Always obtain proper permits and approvals in writing before installing equipment.

Notice: This equipment complies with the limits for class A computing devices in accordance with the specifications in Subpart S of Part 15 of FCC Rules which are designed to minimize radio frequency interference in a residential installation. There is no guarantee that radio or television interference will not occur by activating this equipment. The use of shielded cables or metal conduit may be required in order to reduce radio frequency emissions resulting from the installation wiring.

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## Preface

The N-1000-III/IV Installation Manual provides all information necessary for installation of $\mathrm{N}-1000-\mathrm{III}, \mathrm{N}-1000-\mathrm{III}-\mathrm{X}, \mathrm{N}-1000-\mathrm{IV}$, and $\mathrm{N}-1000-\mathrm{IV}-\mathrm{X}$ control panels.

All of the N-1000-III/IV versions have both the 20 mA and 485 multi-drop communications interfaces.

The N-1000-III/IV can be used in existing N-1000-II/ N-800 systems provided the existing panels have version 8.0 or higher firmware. When the N-1000-II/ N-800 is configured for 485 ( $\mathrm{N}-485-\mathrm{API}-\mathrm{x}$ ), the firmware version must match the 485 version in the N -1000-III/IV.

The following table identifies the different features of each version of the $\mathrm{N}-1000$ panel:

| Version* | Card Readers | MatrixKeypads | Alarm Inputs** | Relay Outputs | Cards*** | Buffers*** |
| :--- | :---: | :---: | :---: | :---: | :---: | ---: |
| N-1000-III | 2 | 2 | 16 | 4 | 5,000 | 10,200 |
| N-1000-III-X | 2 | 2 | 16 | 8 | 25,000 | 6,600 |
| N-1000-IV | 4 | 2 | 16 | 4 | 5,000 | 10,200 |
| N-1000-IV-X | 4 | 2 | 16 | 8 | 25,000 | 6,600 |

* A version with removable wiring terminal strips is available for each model and is designated by an " $R$ " after the version number.
**Two system alarms are independent of the sixteen zone inputs. Matrix keypads do not use any of the 16 alarm inputs.
***These are default values. Capacity can be changed by use of the "I" command U option. Refer to Appendix A: Programming Quick Reference Guide for details.


## New and Improved Features of the $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$

The N-1000-III/IV have both a number of improvements and some unique features as compared to the $\mathrm{N}-1000-\mathrm{II}$ control panel. Some of these features are highlighted below. For a more detailed description refer to Appendix B of this manual.

- Four layer PWB and other improvements provide greater electrical noise immunity.
- High efficiency switching regulators result in reduced heat generation and extended backup battery operation.
- A Four Reader Board allows the N-1000-IV panel to support a total of four readers. (See Section 4-3 for details.) When this board is present all 4 readers attach to it via its removable terminal strips. A Reader Function Test is available on the Four Reader Board by shorting a pair of jumper prongs labeled "TEST." This will cause the board to generate a simulated card read (with the PIC firmware version \#) from each of the four readers.
- An improved canister has more knockouts, a larger battery bracket and improved wiring space, since the board is centered in the enclosure. (The old enclosure can be field retrofitted for the new board.)
- The processor speed is substantially faster than the standard N-1000-II, increasing the speed of internal processing. At the same time, communications output is the same as the $\mathrm{N}-1000$-II panels so they can be used in the same loop.
- An additional Terminal Block (TB9) has been added in the upper left quadrant of the board. This terminal block has an Earth Ground connection; a battery plus and minus (in addition to the soldered-in battery wires); AC connections 1 and 2 (there are no spade lugs for AC or battery); the Tamper and External Power Fail inputs with common posts; and a place to tie the RS-485 shields.
- The N-1000-III/IV firmware (version 08.01 .29 or later) has a built-in test capability that can be used to check the functioning of most of the circuit board's hardware. This feature is detailed in Appendix C: Trouble Shooting.
- There are both RS-485 and RS-232 communications ports in addition to the 20 milliamp port. The RS-485 port is selected by a change of J umper 1 in lieu of the 20 milliamp. The RS-232 port can be used in addition to or instead of the other ports.
- A 16.5 volt AC power source is now required, allowing the charging of a 12 volt battery for back-up as well as the supply of 12 volt reader and PIR egress power from the $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$.
- A 12 volt output 500 mA is available for powering 12 volt readers or PIR egress detectors. Connections for this are at TB9 terminals 7 (+) and 8 (Common).
- Detection of a primary power failure by either the internal sensor or the input from the relay on the external supply will generate an Alarm 19 not Alarm 8. Alarm Point 8 is now available for use as a standard alarm point
- Automatically resetting solid state fuses are used.
- The Green Low-V OK LED will go off to indicate low 12 Volt battery or disconnected RAM back-up.
- A special set of connectors for the AEP-3 Relay Expansion board is provided. J1 is a connector for the present AEP-3 Board design. J 2 is a double connector and will be used for a future development.
- There are screw terminals for the reader cable shield connections. These are nearer to where the wires come in, eliminating the need to run the shield wire around the board.
- The terminals on terminal block 8 are used exclusively for 11 wire Keypads. This makes all regular inputs available even when a keypad is used. The " $K$ " option is no longer needed. The "P" option must still be entered for use with PINs. Screw terminal 4, is used for the Black (Ground) wire of the Keypad.
- There is a separate Tamper Input located on TB-9. This makes Input 12 (terminal now located on TB-6) available as a regular alarm input.
- Each Input can be individually programmed for both Normally Open or Normally Closed and Supervised or Non-Supervised operation.
- There are several changes to the System inputs.

> Alarm 17 Communications failure alarm, but now it reports for either 20 mA or 485 failures.

Alarm 18 Reserved for future reporting of an Auxiliary Communications Failure (the RS-232 port.)
Alarm 19 Primary Power Fail alarm.
Alarm 20 Tamper alarm (with special terminal connections on Terminal Block 9).
Alarm 21 Input Ground Fault alarm. If the input is shorted to Earth Ground this alarm will be generated. (Some fault conditions may generate an ALARM rather than TROUBLE from the point, but no ground fault will be interpreted as NORMAL.)
Alarm 22 Reserved for future use.
Alarm 23 Indicates an external 5 volt reader power short circuit.
Alarm 99 Generated at restart either due to the Push Button or the watch dog timer. (This alarm cannot be stored in the history buffer.) A 99 trouble is communicated on a cold boot-up reset when the RAM memory is being initialized.

- Relays are heavy duty inductive load rated. They have a maximum load rating of 30 VDC, 5 A Resistive, 2 A Inductive. They also have circuitry which is less susceptible to electrical switching noise.


## INTRODUCTION

## Section 1: Access Control

Access control is computerized control over entry to any area that can be secured with a lock and key. Entry is only allowed to authorized people at authorized times. Control of who is allowed to come and go is easily maintained.

The weakness of a lock and key security system is the common key. The key is a readily duplicated piece of metal that gives anyone who holds it access to an area. The risk of lost or stolen keys, with the expense of changing locks, is a costly problem. Access control is an effective and affordable solution to this problem. With access control, each person receives a card or keycode which restricts access to authorized areas at authorized times. A small, programmable control panel allows or denies access. If a card is lost or stolen, or if a keycode is no longer secure, the control panel can be reprogrammed quickly and easily.

An additional benefit of access control is report capability. The system provides reports of all card/ keycode activity, including whether access was granted or denied, and why. A permanent record of all entries to an area can be maintained.

## Section 2: System Overview

## 2-1: N-1000-III/V

The N-1000-III/ IV control panels are the decision-making units in the access control system. Each control panel contains a Central Processing Unit (CPU) and memory for local control capability. Control panel database information is programmed from a central programming device (computer, printer/ programmer or data terminal). The panels then operate independently from the programming device. In this distributed database system, the programming device need not be on-line for system operation.

The control panels interface to the programming device via the C-100-A1 20 mA Current Loop Converter or via the 485 multi-drop interfaces ( $\mathrm{N}-485-\mathrm{PCI}-2$ or N -485-HUB-2). A single communication loop supports up to 63 uniquely addressed control panels if the 20 mA loop is utilized or 31 panels for 485 communications. A typical 20 mA communication loop configuration is shown in figure 2-1 while figure 2-2 illustrates a typical 485 multi-drop configuration.

The N-1000-III control panel provides 16 points for alarm monitoring and four output control relays. The $\mathrm{N}-1000-\mathrm{III}-\mathrm{X}$ control panel includes four additional


Figure 2-1. Typical 20 mA Communications Loop. The N-1000 control panels can be in any order in the loop or dropline as long as each has a unique address.
relays (providing a total of eight) and has expanded database/ buffer memory capacity. The N-1000-IV version has four rather than two card readers and four output control relays. The N-1000-IV-X control panel includes four additional relays ( providing a total of eight) with expanded database/ buffer memory capacity. The $\mathrm{N}-1000-\mathrm{III}-\mathrm{X}$ is shown in figures 2-3 with enclosure and battery. Figure 2-4 shows the N-1000-IV-X with four reader board, battery and enclosure.

Control panels can operate in a buffered mode, in which system transactions are stored in transaction buffer memory at the panels, rather than transmitted to the programming device. Every transaction (input point change of state, code use) uses one location in buffer memory. The buffered information can later be transmitted to the programming device.

The N-1000-III/ IV firmware (version 08.01.29 or later) has a built-in test capability that can be used to check the functioning of most of the circuit board's hardware. Refer to Appendix C: Troubling Shooting for details.

## 2-2: Programming Devices

N-1000-III/IV control panels in a communication loop are programmed with a personal computer, printer/ programmer or data terminal.

When using a personal computer as the system programming device, the PC software serves as a database manager for the N-1000-III/IV control panels. Information is entered into the PC databases and then "downloaded" to the control panels. During the download procedure, the PC software converts the database information at the disk level to a series of command strings from the N -1000 instruction set. The commands are then sent to the appropriate control panels. The panels


Figure 2-2. Typical 485-Multidrop Configuration. The N-1000-III/IV can beused in existing $\mathrm{N}-1000-\mathrm{II} / \mathrm{N}-800$ systems provided the existing panels have version 8.0 or higher firmware. When the N-1000-II/ N-800 is configured for 485 (N-485-API-2) as in this example, the firmware version must match the 485 version in the $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$.


Figure 2-3. $\mathrm{N}-1000-\mathrm{III}-\mathrm{X}$ Panel with Enclosure. The N-1000-III control panel provides 16 alarm points for alarm monitoring capability and four relays for output control capability. TheN-1000-III-X includes four additional relays (providing a total of eight) and has expanded database/ buffer memory capacity.


Figure 2-4. N-1000-IV-X Panel with Enclosure. TheN-1000-IV supports four card readers and four relays for output control capability. The N-1000-IV-X includes four additional relays with expanded database/ buffer memory capacity.
then operate independently from the computer. The advantages of using a computer as the system programming device include ease of programming and operation, permanent disk storage of all database information and extensive report options.

When using a printer/ programmer or data terminal as the system programming device, all command strings are sent manually to the control panels by the operator. Printer/ programmer and data terminal programming provide no safeguard against lost database memory at the panel level.

## 2-3: C-100-A1 Converter

The C-100-A1 Converter serves as the interface betw een the programming device and the $\mathrm{N}-1000$ control panels. The $\mathrm{C}-100-\mathrm{Al}$ allows the programming device, using RS-232 port, to communicate with the control panels in a 20 mA current loop. The C-100-A1 can be configured for use as a $\mathrm{C}-100-\mathrm{C}, \mathrm{C}-100-\mathrm{T}$ or $\mathrm{C}-100-\mathrm{M}$, determined by the position of six DIP switches on the RS-232 connector. For connection from a computer to local control panels, use the C-100-A1 and set it as a C-100-C. For connection from a printer/ programmer to local control panels, set the C-100-A1 as a C-100-T. For connection to remote control panels, via modems, set the C-100-A1 as a C-100-M. Refer to the C-100-A1 Manual for further details.

## 2-4: N-485-PCI-2 or HUB-2

The N-485-PCI-2 is used to interface between a PC's RS-232 port and the 485 multi-drop communications bus. The N-485-HUB-2 is used to interface between a modem's RS-232 port and the 485 multi-drop bus. Only the "-2" versions of these products will function with the $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$.

## 2-5: PROM Versions

The N-1000-III/IV PROM (Programmable Read Only Memory) chips provide permanent storage for the program and control logic information necessary to coordinate and drive the system hardware. The PROM chip is also referred to as the control panel's FIRMWARE.

The PROM version in use determines the commands and programming features available for use with the control panel. Operations such as alarm point programming, site code checking, card/ keycode capacity, transaction buffer capacity, Visitor and Limited Use status cards and local anti-passback are all functions of the PROM version in use. Specific command string syntax and the availability of advanced programming commands are also dependent upon the PROM version.

Refer to the command summary in Appendix A of this manual. Or contact NCI for additional information on PROM versions.

## Section 3: Hardware Specifications

Power Requirements:
The N-1000-III/IV require a $16.5 \mathrm{VAC}, 50 \mathrm{VA}, 60 \mathrm{HZ}$ or 12 VDC linear (2 amp continuous) power supply.

Output Power:
12 VDC ( 10 to 14 volts) 500 mA for readers requiring 12 VDC or motion detector devices (not for use with locking devices). A 5 volt 500 mA output is available for standard 5 VDC reader requirements.

Battery Backup:
The 12 VDC ( $4 \mathrm{amp} / \mathrm{hr}$.) battery provides up to 4 hours of full operational backup (depending on the load and the age of the battery). To maintain the maximum back-up time, the replace the battery every four years or every two years if operating at higher temperatures.

Battery Current Draw:

| Control panel | 300 mA |  | 300 mA |
| :---: | :---: | :---: | :---: |
| Alarm points | 5 mA each | ( x 18 ) | 90 mA |
| Relays activated | 50 mA each | ( x 8 ) | 400 mA |
| 12 V \& readers | 500 mA maximum |  | 500 mA |
| 5 V card readers | 500 mA maximum |  | 500 mA |
| Total | 1.8 amps maximum |  | 1.79 amps |

Memory Backup:
A large value capacitor retains panel memory upon loss of both primary and backup battery power for up to 7 days (depending on ambient temperature and the number of RAM ICs).

Fuses:
3 amp solid-state, non-replaceable, automatic resetting. Depending on the overload, and the temperature, it may take up to several minutes for the fuses to reset.

Ala rm Input Points:
Input points are provided which can be configured for either normally open or normally close and either 3 state supervised or unsupervised. Separate non-supervised inputs are provided for an optional external primary power fail indication and the standard enclosure tamper switch.

Relay Output Points:
Four double pole, double throw (DPDT) relay contacts with both normally-open and normally-closed sides, rated for 30 VDC 2 amp inductive loads. The N-1000-III/IV-X controllers provide four additional relays (eight total). These relays can also be utilized in dry circuit applications (e.g. , mechanical shunts, data interruptions, etc.). NOTE: once the relay pole has been used on an inductive load (door strikes, magnetic locks, etc.) it cannot be used in low current dry circuit applications. Northern recommends using the "A" pole of the relays for inductive loads and the "B" pole for dry circuit (logic) loads.

Operating Temperature:
350 to $110^{\circ} \mathrm{F}$ ( 20 to 430 C ).
Operating Relative Humidity:
Up to $95 \%$ non-condensing.
Enclosure:
14 " h x 16" w x 4"d ( $35.6 \mathrm{~cm} \times 40.6 \mathrm{~cm} \times 10.2 \mathrm{~cm}$ ) with knockouts, hinged cover with lock and key. Enclosure tamper switch provided. The N -1000-III/IV enclosure ( with control panel) is illustrated in figure 3-1.

Weight:
21 pounds ( 9.5 kg ) with enclosure and backup battery.


Figure 3-1. Enclosure for the $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$. The $\mathrm{N}-1000$ enclosure, shown here with the N -1000-IV panel, has a lock and key, knockouts and a tamper switch. A 12 VDC battery is mounted on the door.

INSTALATION

## Section 4: Panel Layout

The N-1000-III-X has nine screw-down terminal blocks. Each terminal block, in turn, has 12 individual terminal positions, described in the following sections. Figure 4-1 shows the panel with its printed template. The N-1000-IV-X has an additional board in place of TB5 (see figure 4-2) with four removable wiring terminal blocks for connection to four readers. Complete terminal block details follow.


Figure 4-1. $\quad \mathrm{N}-1000$-III Panel. The panel is shown with its printed template.

## 4-1: Four Rea der Board (N-1000-IV only)

The N-1000-IV supports up to four card readers. All four readers attach to the panel via a special Four Reader Board mounted in the lower right quadrant of the $\mathrm{N}-1000-\mathrm{IV}$ panel (see figure 4-2). The wiring terminals are removable and interchangeable. The regular reader terminals on the main board are not used. This board is connected to the main panel by connector P3, located directly above the bottom center of the $\mathrm{N}-1000$.

Typical wire color terminations are illustrated in figure 4-3. Refer to the Reader Installation Technical Bulletin (included with the reader) for the most recent color terminations.


Figure 4-2. $\quad \mathrm{N}-1000-\mathrm{IV}$ Panel. The panel is shown with its printed template and the Four Reader Board..

A special, programmable chip, called a PIC chip controls the Four Reader Board. The PIC chip is labeled with a version number similar to the label on the PROM chips on the main board.

The Four Reader Board has its own status LED that flashes when it is powered and running. When a card is read the status LED gives a long flash. When the LED stays on for an extended period of time it indicates that the board has reset due to an error or power supply problem.

A pair of jumper prongs located on the Four Reader Board allows a reader function test. Shorting these prongs (labeled "TEST") will cause the board to generate a simulated card read (with the PIC firmware version \#) from each of the four readers.


Figure 4-3. Four Reader Board. On the N-1000-IV the Four Reader Board replaces terminal block 5 and provides connections for up to four readers.

## 4-2: Terminal Blocks 1, 2, 3, 4

(Terminal Blocks 3 and 4 are available on the $\mathrm{N}-1000$-III/IV-X only)
Each DPDT relay provides control for up to two external devices. Both poles of a given relay have a normally-closed terminal, a common terminal and a normallyopen terminal. (See figure 4-4.) Northern recommends using the "A" pole of the relays for locks (inductive loads) and the "B" pole for dry circuit (logic) application.

Terminal block 1 provides relay contacts for relays \#1 and \#2.
Terminal block 2 provides relay contacts for relays \#3 and \#4.
Terminal block 3 provides relay contacts for relays \#5 and \#6.
Terminal block 4 provides relay contacts for relays \#7 and \#8.
Terminal block 1, 2, 3, 4 terminations are as follow:

| Pos. | TB1 | TB2 | TB3 | TB4 | Function |
| :---: | :--- | :--- | :--- | :--- | :--- |
|  |  |  |  |  |  |
| 1 | Relay \#2:A | Relay \#3:A | Relay \#6:A | Relay \#8:A | Normally-Closed |
| 2 | Relay \#2:A | Relay \#3:A | Relay \#6:A | Relay \#8:A | Common |
| 3 | Relay \#2:A | Relay \#3:A | Relay \#6:A | Relay \#8:A | Normally-Open |
|  |  |  |  |  |  |
| 4 | Relay \#2:B | Relay \#3:B | Relay \#6:B | Relay \#8:B | Normally-Closed |
| 5 | Relay \#2:B | Relay \#3:B | Relay \#6:B | Relay \#8:B | Common |
| 6 | Relay \#2:B | Relay \#3:B | Relay \#6:B | Relay \#8:B | Normally-Open |
|  |  |  |  |  |  |
| 7 | Relay \#1:A | Relay \#4:A | Relay \#5:A | Relay \#7:A | Normally-Closed |
| 8 | Relay \#1:A | Relay \#4:A | Relay \#5:A | Relay \#7:A | Common |
| 9 | Relay \#1:A | Relay \#4:A | Relay \#5:A | Relay \#7:A | Normally-Open |
|  |  |  |  |  |  |
| 10 | Relay \#1:B | Relay \#4:B | Relay \#5:B | Relay \#7:B | Normally-Closed |
| 11 | Relay \#1:B | Relay \#4:B | Relay \#5:B | Relay \#7:B | Common |
| 12 | Relay \#1:B | Relay \#4:B | Relay \#5:B | Relay \#7:B | Normally-Open |



Figure 4-4. Terminal Blocks 1, 2, 3, 4. Terminal blocks 3 and 4 are available on the $X$ versions of the $\mathrm{N}-1000$-III/IV.

## 4-3: Terminal Block 5

Terminal block 5 (N-1000-III only) supports the interface to two Wiegand output card readers and provides an alarm point common. Each card reader port includes terminals for LED control, Data 1 signal, Data Ø signal, + 5 VDC output and Ground. Figure 4-5 illustrates typical wire color terminations. Refer to Reader Installation Tech Bulletin (included with the reader) for most recent color terminations. See Section 6 for specific card reader wiring/ installation details.

| Terminal | Function | Reader Wire Col |
| :---: | :--- | :--- |
|  | Card Reader \#1: LED (Aux. Output \#11) | Brown |
| 2 | Card Reader \#1: Data 1 | White |
| 3 | Card Reader \#1: Data Ø | Green |
| 4 | Card Readers \#1 \& \#2: +5 VDC | Red |
| 5 | Card Readers \#1 \& \#2: Ground | Black |
| 6 | Card Reader \#2: Data Ø | Green |
| 7 | Card Reader \#2: Data 1 | White |
| 8 | Card Reader \#2: LED (Aux. Output \#12) | Brown |
| 9 | Alarm Point Common* |  |
| 10 | LED/Buzzer \#3 (Aux. Output \#13) |  |
| 11 | LED/Buzzer \#4 (Aux. Output \#14) |  |
| 12 | Earth Ground for Cable Shields | Shield |

*All N-1000 alarm point commons are electrically the same and can be used with any panel alarm point.

## 4-4: Terminal Block 6

Terminal block 6 contains alarm input points \#5-16 terminals. Any N-1000 alarm common can be used with the alarm inputs. (See figure 4-5.)

| TB6 Terminal | Function |
| :---: | :--- |
| 1 | Alarm Input \#5 (Door 1 Egress) |
| 2 | Alarm Input \#6 (Door 2 Egress) |
| 3 | Alarm Input \#7 (Door 3 Egress N-1000-IV) |
| 4 | Alarm Input \#8 (Door 4 Egress N-1000-IV) |
| 5 | Alarm Input \#9 |
| 6 | Alarm Input \#10 |
| 7 | Alarm Input \#11 |
| 8 | Alarm Input \#12 |
| 9 | Alarm Input \#13 |
| 10 | Alarm Input \#14 |
| 11 | Alarm Input \#15 |
| 12 | Alarm Input \#16 |



Figure 4-5. Terminal Block 5 and Terminal Block 6. Terminal block 5 is only available on the N-1000-III. It supports the interface to two Wiegand output card readers and provides an alarm point common. Terminal block 6 contains alarm input points \#5-16 terminals. Any N1000 alarm common can be used with the alarm inputs.

## 4-5: Terminal Block 7

Terminal block 7 contains the 485 multi-drop communications connections, terminals for alarm input points \#1 through \#4, an alarm point common and the 20 mA communication loop (receive and transmit) terminals as illustrated in figure 4-6.

NOTE: The yellow communications LED (CR16) can be enabled when using RS-485 interface by shorting TB7-11 and TB7-12 together.

Jumper settings on the panel are used to select either the 485 multi-drop or the 20 mA communications mode. The last panel on the 485 multi-drop cable must be properly configured. Refer to Section 4-9, J umpers, for further information.

The 20 mA communication protocol specifications are as follow:
ASCII text characters
8 data bits
1 stop bit
No parity
(Refer to Section 4-8 for baud rate settings.)

| Terminal | Function |
| :---: | :--- |
| 1 | Alarm Point Common* |

2485 multi-drop $(\mathrm{A}+$ ) Red
3485 multi-drop $(B+) \quad$ Black
4 Alarm Input \#1 (door 1 status)
5 Alarm Input \#2 (door 2 status)
6 Alarm Input \#3 (door 3 status N -1000-IV)
7 Alarm Input \#4 (door 4 status N -1000-IV)
8 Alarm Point Common*
920 mA Communication Loop: Receive+ Red
1020 mA Communication Loop: Receive- Black
1120 mA Communication Loop: Transmit+ White
1220 mA Communication Loop: Transmit- Green
*All N-1000 alarm point commons are electrically the same and can be used with any panel alarm points.

The yellow communications LED (CR16) can be enabled when using RS-485 interface by shorting TB7-11 and TB7-12 together.

## 4-6: Terminal Block 8

Terminal block 8 is used for 11-conductor matrix keypad connections and also provides an alarm point common. See figure 4-7.

See 11-Conductor Keypad instructions (Section 6) for 2 of 7 matrix (row/ column) information.

Keypad Use:
TB8 Terminal Color/Function
$1 \quad$ Brown (Column 1)
$2 \quad$ Blue (Column 2)
$3 \quad$ Green (Column 3)
$4 \quad$ Black (Common)
$5 \quad$ Gray (Row 1)
$6 \quad$ Purple (Row 2)
7 Yellow (Row 3)
$8 \quad$ Orange (Row 4)
$9 \quad$ Peach/Pink ( +5 v )
10 Keypad \#1: White (Select 1)
11 Keypad \#2: White (Select 2)
12 Alarm Point Common*

## 4-7: Terminal Block 9

Terminal block 9 is located just to the right of terminal block 8. It has the power supply and auxiliary connections. See figure 4-7. The 12 volt back-up battery wires are soldered to the circuit board. When connecting an external DC supply the N-1000-III/IV 12 VDC battery can remain connected and provides additional battery backup.
TB9 Terminal Function

| 1 | Transformer Earth Ground (DO NOT USE if panel is already <br> grounded. See Section 8-1.) |
| :--- | :--- |
| 2 | External DC Supply $+(12 \mathrm{~V}$ battery + ) |
| 3 | External DC Supply $-(12 \mathrm{~V}$ battery -) |
| 4 | AC Transformer Wire 1 |
| 5 | AC Transformer Wire 2 |
| 6 | Not Used |
| 7 | +12 V DC Output (500 mA - not for Locking Device) |
| 8 | DC Common* |
| 9 | Tamper Switch Input |
| 10 | Input Common* |
| 11 | External Power Fail Input |
| 12 | 485 Shield (if used) |

*All N-1000 alarm point commons are electrically the same and can be used with any panel alarm points.


Figure 4-7. Terminal Blocks $8 \& 9$. Terminal block 8 is used for 11 -conductor matrix keypad connections and also provides an alarm point common. Terminal block 9 is located just to the right of Terminal block 8. It has the power supply and auxiliary connections.

## 4-8: DIP Switch Settings

N-1000-III/IV DIP switch positions 1 and 2 control the panel baud rate for the 20 mA Loop. Set the panel baud rate to match that of the system programming device. 1200 baud is recommended for computer and data terminal systems. For 485 communications use the 4800 baud setting.

N-1000-III/IV DIP switch positions 3 through 8 determine a control panel's address. (See figure 4-8.) Each control panel in the communication loop must have a unique address to allow unique referencing during system programming. When running the panel's self-test, set all DIP switches to the On position before restarting the panel. (For details of the self-test, see Appendix C: Trouble Shooting.)

Baud rate and panel address DIP switch settings are as follow:

| Baud Rate | 1 | 2 | Panel \# | 3 | 4 | 5 | 6 | 7 | 8 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1200 | Off | Off | 1 | On | On | On | On | On | Off |
| 2400 | Off | On | 3 | On | On | On | On | Off | Off |
| 4800 | On | On | 2 | On | On | On | On | Off | On |
| 9600 | On | Off | 4 | On | On | On | Off | On | On |
|  |  |  | 5 | On | On | On | Off | On | Off |
|  |  |  | 6 | On | On | On | Off | Off | On |
|  |  |  | 7 | On | On | On | Off | Off | Off |
|  |  |  | 8 | On | On | Off | On | On | On |
|  |  |  | 9 | On | On | Off | On | On | Off |
|  |  |  | 10 | On | On | Off | On | Off | On |
|  |  |  | 11 | On | On | Off | On | Off | Off |
|  |  |  | 12 | On | On | Off | Off | On | On |
|  |  |  | 13 | On | On | Off | Off | On | Off |
|  |  |  | 14 | On | On | Off | Off | Off | On |
|  |  |  | 15 | On | On | Off | Off | Off | Off |
|  |  |  | 16 | On | Off | On | On | On | On |
|  |  |  | 17 | On | Off | On | On | On | Off |
|  |  |  | 18 | On | Off | On | On | Off | On |



Figure 4-8. DIP Switches. Switch positions 1 and 2 control the panel baud rate for the 20 mA loop; switch positions 3 through 8 determine the control panel's address. The positions above are set for panel 1 and a baud rate of 2400 .

|  | Panel \# | 3 | 4 | 5 | 6 | 7 | 8 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 19 | On | Off | On | On | Off | Off |
|  | 20 | On | Off | On | Off | On | On |
|  | 21 | On | Off | On | Off | On | Off |
|  | 22 | On | Off | On | Off | Off | On |
|  | 23 | On | Off | On | Off | Off | Off |
|  | 24 | On | Off | Off | On | On | On |
|  | 25 | On | Off | Off | On | On | Off |
|  | 26 | On | Off | Off | On | Off | On |
|  | 27 | On | Off | Off | On | Off | Off |
|  | 28 | On | Off | Off | Off | On | On |
|  | 29 | On | Off | Off | Off | On | Off |
|  | 30 | On | Off | Off | Off | Off | On |
|  | 31 | On | Off | Off | Off | Off | Off |
| NOTE: Addresses 32 and above cannot be used with the 485 multi-drop. | 32 | Off | On | On | On | On | On |
|  | 33 | Off | On | On | On | On | Off |
|  | 34 | Off | On | On | On | Off | On |
|  | 35 | Off | On | On | On | Off | Off |
|  | 36 | Off | On | On | Off | On | On |
|  | 37 | Off | On | On | Off | On | Off |
|  | 38 | Off | On | On | Off | Off | On |
|  | 39 | Off | On | On | Off | Off | Off |
|  | 40 | Off | On | Off | On | On | On |
|  | 41 | Off | On | Off | On | On | Off |
|  | 42 | Off | On | Off | On | Off | On |
|  | 43 | Off | On | Off | On | Off | Off |
|  | 44 | Off | On | Off | Off | On | On |
|  | 45 | Off | On | Off | Off | On | Off |
|  | 46 | Off | On | Off | Off | Off | On |
|  | 47 | Off | On | Off | Off | Off | Off |
|  | 48 | Off | Off | On | On | On | On |
|  | 49 | Off | Off | On | On | On | Off |
|  | 50 | Off | Off | On | On | Off | On |
|  | 51 | Off | Off | On | On | Off | Off |
|  | 52 | Off | Off | On | Off | On | On |
|  | 53 | Off | Off | On | Off | On | Off |
|  | 54 | Off | Off | On | Off | Off | On |
|  | 55 | Off | Off | On | Off | Off | Off |
|  | 56 | Off | Off | Off | On | On | On |
|  | 57 | Off | Off | Off | On | On | Off |
|  | 58 | Off | Off | Off | On | Off | On |
|  | 59 | Off | Off | Off | On | Off | Off |
|  | 60 | Off | Off | Off | Off | On | On |
|  | 61 | Off | Off | Off | Off | On | Off |
|  | 62 | Off | Off | Off | Off | Off | On |
|  | 63 | Off | Off | Off | Off | Off | Off |

NOTE: For DIP switches with OPEN/CLOSED notation:
OPEN = Off
CLOSED=On

NOTE: The restart button MUST be pressed to activate a change made to any DIP switch setting (for baud rate and/ or panel address). Pressing the restart button DOES NOT alter N-1000-III/IV database memory.

## 4-9: Jumpers

There are several jumpers on the circuit board (figure 4-9) which configure the panel for various modes of operation as indicated below:

| Number | Position | Function |
| :---: | :---: | :---: |
| JP1 | Jump Pins 1 \& 2 | Selects 20 mA communications loop (default) |
|  | Jump Pins 2 \& 3 | Selects 485 Multi-drop Communications |
| JP2 | Open | Jumper on if the $\mathrm{N}-485-\mathrm{PCI}-2$ is not at the end of the multi-drop cabling (JP4 must also be removed if the panel is not at the end). Leave |
| JP 3 | Open | open if $\mathrm{N}-485-\mathrm{PCI}$ is at the end of the dropline. |
| JP 4 | Jump Pins 1 \& 2 | Both jumpers are inserted for the panel at the end of the 485 multi-drop cable (default) |
|  | Remove | Remove the jumper if the panel is not at the end of the cable or JP2 and JP3 are used. |

JP 5 (Not Used)

JP6 Jump pins 2 \& $3 \quad$ Onboard power fail detection selected (default).
Jump Pins $1 \& 2$ Select External power fail detection.

JP 7 (Not Used)

JP 8 (Not Used)
JP9 Jump Pins $1 \& 2$ Disables clearing of RAM by JP10 (default). Remove Enables clearing the RAM by JP10.

JP 10 Jump Pins $1 \& 2 \quad$ Clears RAM when power is off for at least 60 seconds and JP9 is removed.
Remove Will not clear RAM, allows supercap to backup memory (default).

JP 11 (Not Used)

4-10: Connectors

| Connector | Function |
| :--- | :--- |
| Battery Wires | Red and Black wires soldered to the circuit board with fast-on <br> connectors for the 12 volt backup battery |
| J1 | Four pin connector for the AEP-3 (Revision A) Relay <br> Expansion Board(s) |
| J2 | Eight pin connector for the AEP-3 (Revision B) Relay <br> Expansion Board(s) |



Figure 4-9. Configuration Jumpers and Connectors. The N-1000-III/IV panels provide for either 485 multi-drop communication or 20 mA communication loops. These modes are selected by changing jumper settings.

## 4-11: LEDs

The functions of the N-1000-III/ IV LEDs are listed below and also illustrated in figure 4-10. When an LED is lit it indicates that its relay is energized, that is the normally open relay contacts become closed and the normally closed relay contacts become open. Not lit indicates relay is de-energized, that is normally open and normally closed contacts are in normal state.

| Function | LED | Color | Lit Indicates |
| :--- | :--- | :--- | :--- |
| Output Relay \#1 Indicator: | 1 | Red | Energized |
| Output Relay \#2 Indicator: | 2 | Red | Energized |
| Output Relay \#3 Indicator: | 3 | Red | Energized |
| Output Relay \#4 Indicator: | 4 | Red | Energized |
| Output Relay \#5 Indicator: | 5 | Red | Energized |
| Output Relay \#6 Indicator: | 6 | Red | Energized |
| Output Relay \#7 Indicator: | 7 | Red | Energized |
| Output Relay \#8 Indicator: | 8 | Red | Energized |
| 485 Status | 9 | Green | Slow flashing indicates proper <br> 485 communications. |
| +12 VDC Indicator: | 10 | Green | 12 V DC external power is available |
| +5 VDC Indicator: | 11 | Green | Panel is supplying +5 VDC output <br> for reader/keypad power |
| Low-Voltage OK: | 12 | Green | Battery voltage acceptable <br> Input Ground: <br> Run: |
| 13 Red fault detected |  |  |  |



Figure 4-10. LED Functions. When an LED is lit it indicates that its relay is energized, that is, the normally open relay contacts become closed and the normally closed relay contacts become open. Not lit indicates relay is de-energized, that is normally open and normally closed contacts are in normal state.

## 4-12: Restart Button

The restart button is used to restart the N-1000-III/IV microprocessor. (See figure 4-11.) Press the restart button to restart a "locked-up" control panel and to activate a change made to any DIP switch setting (for baud rate and/ or panel address).

The panel's self-test feature can be activated by setting all of the DIP switches to the on position and then depressing the Restart button. For details of the selftest, refer to Appendix C: Troubling Shooting.

Pressing the restart button DOES NOT alter N-1000-III/IV database memory. The data base can be completely cleared by the following method: Disconnect the AC power and battery back-up. Move jumper JP9 to position JP10 for one minute then return it to JP9. Restore the power connections.

## 4-13: RAM Chip

Control panel RAM chips store all database and transaction buffer memory. The $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$ control panels use a single RAM chip with the option of an additional chip in socket U6. The N-1000-III/ IV-X control panels come equipped with an extra RAM chip. (See figure 4-11.)

NOTE: Control panel card database and transaction buffer capacities are determined by the number of RAM chips used and can be modified by use of the " $U$ " command I option. Refer to Appendix A: Programming Quick Reference Guide for details.

## 4-14: PROM Chip

The control panel PROM chips store all N-1000-III/IV program and control logic memory and are located in sockets U5 \& U19. (See figure 4-11.) On each PROM is a sticker which indicates the socket number and the firmware version of the chip. Refer to the main PROM number when referencing the $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$ Programming Manual for specific programming/ operation functions.

## 4-15: Additional Installation Information:

Northern Computers recommends the following installation techniques for the N -1000-III/IV panel.

1. All wiring of door locks/ strikes and panel primary power should be in a separate conduit or there should be at least 12" of space between the power cables and the data/ reader cables.
2. Do not mount the power supplies, modem, or external relays inside the N 1000 enclosure. An empty enclosure (ENC-0 or ENC-2) is available for this purpose. It is the same size and color as the N-1000 enclosure, with the same locking mechanism.
3. Do not "string" wire across the face of the N-1000 panel.
4. Do not use the same power supply for both locks and control panel or locks and readers.
5. Do not mount the control panel in or near an area that has electric noise (e.g. next to a large electric motor or power transformer).
6. The use of shielded cables or metal conduit may be required in order to reduce interfering radio frequency emissions.


Figure 4-11. Additional Panel Features. The restart button is used to restart the N-1000-III/IV microprocessor. It can be used to restart a "locked-up" control panel or to activate a change made to any DIP switch setting (for baud rate and/ or panel address). The N-1000-III/ IV-X control panel comes equipped with an additional RAM chip, located in socket U6. The control panel PROM chips, located in sockets U5 \& U19, store all N-1000-III/IV program and control logic memory.


Figure 4-12. Example Wiring Diagram. Card Readers connect to TB5 of the N-1000-III as shown here. On the N-1000-IV, the card readers connect to the Four Reader Board. See Section 43 for typical wiring diagram.

## Section 5: Operation

## 5-1: Card Reader/Keypad Operation

Some card readers require that a format (software) command be programmed into the host $\mathrm{N}-1000$-III/IV controller before cards can be read. If the format command is not programmed into the control panel, these readers or keypads will not transmit card numbers to the terminal/ printer. See Appendix A: Programming Quick Reference Guide for complete "F" Command format listings.

Verify card reader/ keypad operation before programming. Codes need not be programmed into memory to verify reader/keypad operation. When a code is entered or a card presented, it should appear on the display/ printer, followed by an NF or not found message.

The control panel continuously monitors the card reader and keypad ports for code use. Access is restricted by placing time zone limitations on the codes in use, NOT by time zone controlling (disabling) the reading devices.

Card reader or keypad activation of output points is determined by the presence or absence of the anti-passback option in panel memory. See "I" Command, A Option in Appendix A for further information.

In anti-passback configurations with two card readers or two keypads, both devices activate output relay \#1 (door \#1) upon valid code use (figure 5-1). In anti-passback applications requiring a separate output relay for the reader (such as turnstile applications), refer to " A " Command in Appendix A .

In configurations WITHOUT anti-passback enabled, card reader or keypad \#1 activates output relay \#1 (door \#1) and card reader or keypad \#2 activates output relay \#2 (door \#2) (figure 5-2).

Card readers and keypads can be reassigned to activate a specified input point, output point or group (multiple outputs) upon valid code use, via the "A" Command. Refer to Appendix A.


Figure 5-1. Anti-passback with Two and Four Card Readers. (Keypads may be substituted for card readers on TB8). In anti-passback configurations with two card readers, both devices activate output relay \#1 (door \#1) upon valid code use. With four card readers, readers \#1 and \#2 activate output relay \#1 (door \#1); reader \#3 and \#4 activate output relay \#3 (door \#2). In anti-passback applications requiring a separate output relay for each reader (such as turnstile applications) refer to the N-1000 Programming Manual, "A" Command.


Figures 5-2. Two and Four Card Readers without Anti-passback. In configurations WITHOUT anti-passback enabled, card reader \#1 activates output relay \#1 (door \#1) and card reader or keypad \#2 activates output relay \#2 (door \#2). With four readers, card readers \#1, 2,3 , and 4 activate output relays \#1, 2, 3, and 4 activating doors \# 1, 2, 3, and 4 respectively.

## 5-2: Ala m Input Points

All N-1000 alarm input points default to normally-closed, non-supervised circuits used to monitor changes of state. $\mathrm{N}-1000$-III/IV inputs can also be configured for normally open circuits and 3 -state supervised circuits. Input points have both a physical state and a software state, as described below:

Physic al State
Input points have physical states of OPEN and CLOSED. An open input is considered to be in ALARM condition. A closed input is considered to be in NORMAL condition.

Software State
Input points have software states of UNSHUNTED (active) and SHUNTED (not active). When an input is unshunted, all physical changes of state (openings and closures) are recognized. When an input is shunted, physical changes of state are

## N-1000-III/IV Input Configurations



Figure 5-3. Input point configuration.
not recognized. Input shunting is software controlled and does not involve a physical change of state of the input.

The default software state of all input points is unshunted. No programming is necessary to keep inputs in the default state. Programming is only necessary to shunt input points.

Input points are assigned both shunt time and time zone parameters, as described below:

## Shunt Time

The shunt time parameter defines the amount of time the input point is shunted (deactivated) when triggered, such as upon valid code use.

Time Zone
The time zone parameter defines the time the input point is automatically shunted (deactivated).

## 5-3: Relay Output Points

All N -1000-III/ IV relay output points have both normally-open and normallyclosed contacts, used to switch (activate/ deactivate) electrical devices. Output points have only a physical state, as described below:

## Physic al State

Output points have physical states of DE-ENERGIZED and ENERGIZED. When an output is de-energized, normally-open and normally-closed contacts are in normal state. When an output is energized, normally-open contacts become closed and normally-closed contacts become open.

The default state of all output points is de-energized. No programming is necessary to keep outputs in the default state. Programming is only necessary to energize output points.

Door locks MUST be wired such that the following conditions are met:

1. DE-ENERGIZED relay outputs (default state) result in LOCKED doors.
2. ENERGIZED relay outputs result in UNLOCKED doors.

NOTE: The appropriate side of the relay contact ( normally-open or normallyclosed) MUST be used to satisfy the conditions stated.

Output points are assigned both pulse time and time zone parameters, as described below:

## Pulse Time

The pulse time parameter defines the amount of time the output point is energized when triggered, such as upon valid code use.

## Timezone

The time zone parameter defines the time the output is automatically energized.
Outputs \#11 \& 12 control card reader \#1 and \#2 LEDs, respectively. On the N-1000-IV panel, outputs \#13 \& 14 control card reader \#3 \& 4 LEDs, respectively. Card reader LEDs change state upon valid code use for the duration of the programmed pulse time. Default pulse time for LED outputs is two seconds. See Appendix A, "I" Command, M Option and " V " Command for further card reader LED information.

## 5-4: Default Input Point/Outp ut Point Interaction

The actions of all input points and output points are independent of one another, unless otherwise programmed. Selected inputs and outputs can be programmed to interact with one another through an interlocking option (see Appendix A, " P " Command as well as "I" Command, E Option). Interlocking allows an input point or output point to take a specified action, based upon another input point or output point change of state.
The reserved input and output points, default interlocks, default pulse times and default shunt times for various system configurations (without anti-passback, with anti-passback and with free egress) are shown below:

Configurations WITHOUTAnti-Pa ssback

| Input Point | Reserved for: | Default shunt time |
| :--- | :--- | :--- |
| Input \#1 | Door position switch for door \#1 | 15 seconds |
| Input \#2 | Door position switch for door \#2 | 15 seconds |
| Input \#3* | Door position switch for door \#3 | 15 seconds |
| Input \#4* | Door position switch for door \#4 | 15 seconds |

Output Point Reserved for: Default pulse time

| Output \#1 | Door lock for door \#1 | 10 seconds |
| :--- | :--- | :--- |
| Output \#2 | Door lock for door \#2 | 10 seconds |
| Output \#3* | Door lock for door \#3 | 10 seconds |
| Output \#4* | Door lock for door \#4 | 10 seconds |
|  |  |  |
| Output \#11 | Reader 1 LED | 2 seconds |
| Output \#12 | Reader 2 LED | 2 seconds |
| Output \#13* | Reader 3 LED | 2 seconds |
| Output \#14* | Reader 4 LED | 2 seconds |

[^0]Output \#1 is interlocked to input \#1. An activation of output \#1 (such as upon valid code use at card reader/ keypad \#1) causes input \#1 to be shunted for the duration of its shunt time.

Similarly, outputs \#2, \#3 and \#4 are interlocked to inputs \#2, \#3 and \#4 respectively. Activation of an interlocked output causes the respective interlocked input to be shunted for the duration of its shunt time. (See figure 5-4.)


Figures 5-4. Interlocked Input/ Output on Doors Without Anti-passback.
Example: Valid code use at card reader (or keypad) \#1 triggers the pulse time of output relay \#1, unlocking door \#1 for 10 seconds and pulsing the reader LED for 2 seconds.
The activation of output \#1 triggers the shunt time of input \#1 (via interlock) shunting door \#1 status switch for 15 seconds.

Configurations WITH Anti-Pa ssback:

| Input Point | Reserved for: | Shunt Time |
| :--- | :--- | :--- |
| Input \#1 | Door position switch for door \#1 | 15 seconds |
| Input \#3* | Door position switch for door \#2 | 15 seconds |
| Output Point | Reserved for: | Pulse Time: |
| Output \#1 | Door lock for door \#1 | 10 seconds |
| Output \#3* | Door lock for door \#2 | 10 seconds |

Output \#1 is interlocked to input \#1. Activation of output \#1 causes input \#1 to be shunted for the duration of its shunt time.

Similarly, on the N-1000-IV only, output \#3 is interlocked to input \#3. Activation of output \#3 causes input \#3 to be shunted for the duration of its shunt time. (See figure 5-5.)

## Configurations With Free Egress:

| Input Point | Reserved for: | Default shunt time |
| :--- | :--- | :--- |
| Input \#1 | Door position switch for door \#1 | 15 seconds |
| Input \#2 | Door position switch for door \#2 | 15 seconds |
| Input \#3* | Door position switch for door \#3 | 15 seconds |
| Input \#4* | Door position switch for door \#4 | 15 seconds |
| Input \#5 | Egress device for door \#1 | 0 seconds (N/A) |
| Input \#6 | Egress device for door \#2 | 0 seconds (N/A) |
| Input \#7* | Egress device for door \#3 | 0 seconds (N/A) |
| Input \#8* | Egress device for door \#4 | 0 seconds (N/A) |
| Output Point | Reserved for: | Default pulse time |
|  |  |  |
| Output \#1 | Door lock for door \#1 | 10 seconds |
| Output \#2 | Door lock for door \#2 | 10 seconds |
| Output \#3* | Door lock for door \#3 | 10 seconds |
| Output \#4* | Door lock for door \#4 | 10 seconds |

Input \#5 is interlocked to output \#1. An activation of input \#5 (via egress attempt) causes output \#1 to energize for the duration of its pulse time.

Input \#6 is interlocked to output \#2. An activation of input \#6 (via egress attempt) causes output \#2 to energize for the duration of its pulse time.

Output \#1 is interlocked to input \#1. An activation of output \#1 (such as upon valid code use at card reader/ keypad \#1 or egress attempt) causes input \#1 to be shunted for the duration of its shunt time.

Output \#2 is interlocked to input \#2. An activation of output \#2 (such as upon valid code use at card reader/ keypad \#2 or egress attempt) causes input \#2 to be shunted for the duration of its shunt time. (See figure 5-6.)
*N-1000-IV only


Figures 5-5 Interlocked Input/ Output Points on Door WITH Anti-passback. Example: Valid code use at either card reader (or keypad) triggers the pulse time of output relay \#1, unlocking door \#1 for 10 seconds. The activation of output \#1 triggers the shunt time of input \#1 (via the interlock), shunting door \#1 status switch for 15 seconds.


Figures 5-6 Door \#1 with Free Egress. Valid code use at card reader (or keypad) \#1 OR egress attempt (at input \#5) triggers the pulse time of output relay \#1, unlocking door \#1 for 10 seconds. The activation of output \#1 triggers the shunt time of input \#1 (via the interlock), shunting door \#1 status switch for 15 seconds. Input \#1 is shunted for the duration of its shunt time ( 15 seconds by default) upon valid code use OR upon egress attempt. Door \#1 shunt time is determined by input \#1 shunt time, NOT input \#5 shunt time. Input \#5 shunt time does not apply.


Figures 5-7 Door \#2 with Free Egress. Valid code use at card reader (or keypad) \#2 OR egress attempt (at input \#6) triggers the pulse time of output relay \#2, unlocking door \#2 for 10 seconds. The activation of output \#2 triggers the shunt time of input \#2 (via the interlock), shunting door \#2 status switch for 15 seconds. Input \#2 is shunted for the duration of its shunt time ( 15 seconds by default) upon valid code use OR upon egress attempt. Door \#2 shunt time is determined by input \#2 shunt time, NOT input \#6 shunt time. Input \# 6 shunt time does not apply.


Figures 5-8. Door \#3 with Free Egress. Valid code use at card reader \#3 OR egress attempt (at input \#7) triggers the pulse time of output relay \#3, unlocking door \#3 for 10 seconds. The activation of output \#3 triggers the shunt time of input \#3 (via the interlock), shunting door \#3 status switch for 15 seconds. Input \#3 is shunted for the duration of its shunt time ( 15 seconds by default) upon valid code use OR egress attempt. Door \#3 shunt time is determined by input \#3 shunt time, NOT input \#7 shunt time. Input \#7 shunt time does not apply.


Figures 5-9. Door \#4 with Free Egress. Valid code use at card reader \#4 OR egress attempt (at input \#8) triggers the pulse time of output relay \#4, unlocking door \#4 for 10 seconds. The activation of output \#4 triggers the shunt time of input \#4 (via the interlock), shunting door \#4 status switch for 15 seconds. Input \#4 is shunted for the duration of its shunt time ( 15 seconds by default) upon valid code use OR egress attempt. Door \#4 shunt time is determined by input \#4 shunt time, NOT input \#8 shunt time. Input \#8 shunt time does not apply.

## 5-5: Auto-Relock Operation

The auto-relock feature results in the immediate re-locking and re-arming (unshunting) of a door when the door status switch is closed after entry. With auto-relock operation, an output relay (controlling a door lock) de-energizes when the corresponding input point (door status switch) returns to normal state, and the input point is re-armed (unshunted) rather than remaining energized (shunted) for the duration of its pulse/ shunt time.

Auto-relock associations between inputs and outputs are NOT considered interlocks and do not appear as interlocks on input point reports and output point reports.

Auto-relock operation can be set manually with the " V " Command, K option found in Appendix A.

Default auto-relock operation is described below and illustrated in figure 5-10, 511 and 5-12.

With Anti-Pa ssback
With anti-passback enabled, input \#1 is configured for auto-relock with output \#1.

Without Anti-Pa ssback
Without anti-passback enabled, input point \#1 is configured for auto-relock with output \#1 and input \#2 is configured for auto-relock operation with output \#2.

## 5-6: Timezone-Controlled Doors

When timezone-controlling a door (for free access) that is operated by a card reader or keypad, the time zone must be assigned to the relay output point.

Example:
Want to keep door \#1 (controlled by card reader \#1) unlocked during timezone \#5, defined as 9 AM to 6:30 PM , Monday through Friday.

Timezone \#5 must be assigned to output \#1 (to unlock the door during the timezone).

The default interlock to shunt the door status switch will prevent an alarm from occurring while the output is pulse/ energized.


Figure 5-10. Auto-relock with Anti-Passback. Example: Valid code use at either card reader (or keypad) triggers the pulse time of output relay \#1, unlocking door \#1 for 10 seconds. The activation of output \#1 triggers the shunt time of input \#1 (via the interlock), shunting door \#1 status switch for 15 seconds. Output relay \#1 de-energizes immediately upon input \#1's return to normal state, relocking door \#1 and re-arming input \#1, rather than remaining energized (shunted) for the duration of the 10 second pulse time/ 15 second shunt time.


Figure 5-11. Auto-relock Doors \#1 and 2 without Anti-passback. Output relay \#1 de-energizes immediately upon input point \#1's return to normal state, re-locking door \#1 and re-arming input \#1, rather than remaining energized (shunted) for the duration of the 10 second pulse time/ 15 second shunt time. Output relay \#2 de-energizes immediately upon input point \#2's return to normal state, re-locking door \#2 and re-arming input \#2, rather than remaining energized (shunted) for the duration of the 10 second pulse time/ 15 second shunt time.


Figure 5-12. Auto-relock Doors \#3 and \#4 without Anti-passback. Output relay \#3 deenergizes immediately upon input point \#3's return to normal state, relocking door \#3 and re-arming input \#3, rather than remaining energized (shunted) for the duration of the 10 second pulse time/ 15 second shunt time. Output relay \#4 de-energizes immediately upon input point \#4's return to normal state, re-locking door \#4 and rearming input \#4, rather than remaining energized (shunted) for the duration of the 10 second pulse time/ 15 second shunt time.

## Section 6: Wiring Requirements

## 6-1: Card Readers

Some card readers require that a format (software) command be programmed into the host $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$ controller before cards can be read. When a card is run through a card reader that requires a format command and the format command has not been programmed into the control panel, no card number is transmitted to the terminal/ printer. See Appendix A for complete "F" command format listings.

N-1000-III Terminal Block 5 supports interface to two Wiegand output card readers and provides an alarm point common. Each card reader port includes terminals for LED control, Data 1 signal, Data Ø signal, + 5 VDC output and Ground. See figure 61 for typical wiring diagram.*

Card reader/ keypad communication shields should be connected to TB5 position 12. The shield at the card reader/ keypad end should not be grounded unless the device is electrically floating (not touching metal). (See figure 6-1.)

Typical Card Readers using Reader Cable NC1861 or for Plenum applications NCP1861 have the following terminations (maximum distance is 500' or 152 m ). Refer to documentation included with your reader for the latest information. (Cable specifications and part numbers are listed at the end of this section.)

Reader \#1:
Terminal

| Block | Terminal | Function | Reader WireColor |
| :---: | :---: | :--- | :--- |
| 5 | 1 | LED (Aux. Output \#11) | Brown |
| 5 | 2 | Data 1 | White |
| 5 | 3 | Data 0 | Green |
| $5($ or 9$)$ | 4 (or 7) | +5 VDC (+12 VDC)* | Red |
| 5 | 5 | Ground | Black |
| 5 | - | Not used | Blue |
| 5 | 12 |  | Shield |

Reader \#2:
Terminal

| Block | Terminal | Function | Reader WireColor |
| :---: | :--- | :--- | :--- |
| 5 (or 9$)$ | 4 (or 7$)$ | +5 VDC $(+12$ VDC)* | Red |
| 5 | 5 | Ground | Black |
| 5 | 6 | Data 0 | Green |
| 5 | 7 | Data 1 | White |
| 5 | 8 | LED (Aux. Output \#12) | Brown |
|  | - | Not used | Blue |
| 5 | 12 |  | Shield |

*+12 VDC 500 mA power is available on Terminal Block 9 position 7. Use TB5 terminal 4 OR TB9 terminal 7 NOT BOTH.


Figure 6-1. Card Reader Connections to TB5. N-1000-III Terminal Block 5 supports interface to two Wiegand output card readers and provides an alarm point common. Each card reader port includes terminals for LED control, Data 1 signal, Data $\varnothing$ signal, +5 VDC output and Ground. +12 VDC power is available on Terminal Block 9 position 7. Card reader/ keypad communication shields should be connected to TB5 position 12. The shield at the card reader/ keypad end should not be grounded unless the device is electrically floating ( not touching metal).

## 6-2: Four Reader Board (N-1000-IV Only)

The N-1000-IV includes a board that supports up to four card readers. The wiring terminal blocks are removable and interchangeable. No connections should be made to the TB5 terminals of the main board when the Four Reader Board is connected. (See figure 6-2.)
If a reader requires 12 VDC, power can be obtained from TB9 terminal 7. The electrical load on TB9 terminal 7 must not exceed 500 mA . If the combined reader current draw exceeds 500 mA , a separate power supply should be used. Refer to the Readers'Technical Bulletin for the wiring details.

Reader \#1, 2, 3, 4:

| Terminal | Function | Reader WireColor |
| :--- | :--- | :--- |
| 1 | LED (Aux. Output \#11) | Brown |
| 2 | Data 1 | White |
| 3 | Data $\varnothing$ | Green |
| 4 (TB9-7) | +5 VDC $(+12$ VDC)* | Red |
| 5 | Ground | Black |
| - | Not used | Blue |

*+12 VDC power is available on Terminal Block 9 terminal 7. Use connection 4 or TB9 terminal 7 NOT BOTH.


Figure 6-2. Card Reader Connections for the N-1000-IV. TheN-1000-IV includes a board that supports up to four card readers. The wiring terminals are removable and interchangeable. No connections should be made to the TB5 terminals of the main board when the Four Reader Board is connected.

On the N-1000-IV, card reader/ keypad communication shields should be connected to the panel at the shield position on the Four Card Reader wiring blocks. The shield at the card reader/ keypad end should not be grounded unless the device is electrically floating (not touching metal). (See figure 6-3.)


Figure 6-3. Card Reader/Keypad Grounding N-1000-IV. Card reader/ keypad communication shields should be connected to the panel at wiring block position 5. The shield at the card reader/ keypad end should not be grounded unless the device is electrically floating (not touching metal).

## 6-3: Eleven Conductor Keypads

A typical Eleven Conductor Keypad uses a 2 of 7 matrix configuration where each keypad row and column corresponds to a unique wire (Figure 6-4). When a button is pressed, two of seven wires are activated (two of the wires are put to ground); one wire corresponds to the button's row and one wire corresponds to the button's column. For example, if the " 3 " button is pressed, the gray (row 1 ) and green ( column 3) wires are activated (put to ground). If the " 8 " button is pressed, the yellow (row 3) and blue (column 2) wires are activated (put to ground).

Typical eleven conductor keypads, using Keypad Cable NC18121 or for Plenum applications NCP18121 (maximum distance is 500' or 152 m ), have the following terminations (See figure 6-5):

| Keypad \#1 | Keypad \#2 |  |  |  |  |
| :--- | :--- | :--- | :---: | :---: | :--- |
| TBTerminal | WireColor | TB | Terminal | WireColor |  |
| 8 | 1 | Brown | 8 | 1 | Brown |
| 8 | 2 | Blue | 8 | 2 | Blue |
| 8 | 3 | Green | 8 | 3 | Green |
| 8 | 4 | Black | 8 | 4 | Black |
| 8 | 5 | Gray | 8 | 5 | Gray |
| 8 | 6 | Purple | 8 | 6 | Purple |
| 8 | 7 | Yellow | 8 | 7 | Yellow |
| 8 | 8 | Orange | 8 | 8 | Orange |
| 8 | 9 | Peach/ Pink | 8 | 9 | Peach/Pink |
| 8 | 10 | White | 8 | 11 | White |
| 5 | 4 | Red | 5 | 4 | Red |

Refer to your keypad technical documentation for the latest information.


Figure 6-4. Eleven Conductor Keypad 2 of 7 Matrix.

Tan Earth Ground wire connections (Not used on all keypads). If the keypad is mounted directly on grounded metal, tie the tan wire to the grounded metal and tie the keypad cable shield to the $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$ grounding point (float shield at keypad).

If the keypad is mounted on ungrounded metal or nonmetallic material, tie the tan wire to the keypad cable's shield only and connect the shield to the N-1000-III/IV ground point.


Figure 6-5. Eleven Conductor Keypad Connection to the N-1000-III/IV. If the keypad is mounted on ungrounded metal or nonmetallic material, tie the tan wire to the keypad cable's shield only and connect the shield to the N-1000-III/ IV ground point.

## 6-4: Ala m Input Points

Panel/ communication cable NC1821/ NCP1821 (twisted, 18 gauge overall shielded cable) is recommended for alarm input point use. This enables the input device to be located up to 2000 feet ( 610 m ) away from the $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$.

Connect the shield to the panel's earth ground and leave the other end of the shield not connected.

All N -1000-III/IV alarm point commons are electrically the same and can be used with any panel alarm point. See terminal block diagrams in Section 4 for alarm point common locations.

If end of line (EOL) resistors are used for supervised inputs, then they must be located at the switch in order to be effective.

See Appendix A for a summary list of specific alarm input point commands.

## $6-5$ : Relay Output Points

Panel/ communications cable NC1821/ NCP1821 (twisted pair, 18 gauge, overall shielded) is recommended for relay output point use. This enables the output device to be located up to 2000 feet ( 620 m ) away from the N-1000-III/ IV. An 18 gauge wire's resistance results in a 6.5 volt drop for a quarter ampere load at 2000 feet.

See Appendix A for a summary list of specific relay output point commands.

## 6-6: Communications

## 6-6-1: PC to C-100-A1

Cable CBL-2 (three conductor, 18-20 gauge) is recommended for use between the PC and the C-100-A1 Converter. This enables the C-100-A1 to be located up to 50 feet ( 15 m ) away from the CPU (See figure 6-6).

## 6-6-2: C-100-A1 to Panel(s)

Panel/ communications cable NC1821/ NCP1821 (twisted pair, 18 gauge shielded) is recommended, enabling runs of up to 2000 feet ( 610 m ), for each of the following:

C-100-A1 to first panel in communication loop.
Panel to panel.
Last panel in communication loop to C-100-A1. (See figure 6-6.)
The 20 mA communications loop for the C-100-A1 and the $\mathrm{N}-1000$-III/IV operate using active-transmit and passive-receive. The Communications Type Selection J umper J P1 must be set to the 20 mA (default) position $1 \& 2$.

In a single panel 20 mA communication loop, ground the communication cable shield only at the panel, as shown in figure 6-8.

In a multiple 20 mA panel communication loop, the 20 mA communication cable shield between any two given panels in the loop should be grounded at one end only. Ground either the receive side OR the transmit side of the shield (but NOT both), giving care to be consistent throughout the loop. The receive side grounding method is preferred (see figure 6-9). The transmit side grounding method is not recommended.

## 6-6-3: AEP to $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$

Up to two AEP-3 Relay Expansion Boards per N-1000-III/IV panel can be added to the communication loop. Using the cable supplied with the unit, one AEP-3 connects to the $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$ at J 1, and then to the next AEP-3. (See figure 6-10.) Refer to the AEP-3 manual for further details.


Figure 6-6. Typical 20 mA Communication Loop. See figure 6-10 for AEP-3 connections.


Figure 6-7. Typical C-100-A1 Multiple Panel 20 mA Communication Loop. Refer to figure 6-9 for grounding of the communication loop. The Communications Type Selection J umper J P1 must be set to the 20 mA (default) position $1 \& 2$.


Figure 6-8. Grounding a Single Panel, 20 mA Communication Loop. In a single panel 20 mA communication loop, ground the communication cable shield only at the panel.


Figure 6-9. Grounding a Multiple Panel, 20 mA Communication Loop. In a multiple 20 mA panel communication loop, the 20 mA communication cable shield between any two given panels in the loop should be grounded at one end only. The preferred receive side grounding method is shown here. The transmit side grounding method is not recommended.


Figure 6-10. Connecting an AEP-3 Relay Expansion Board. TheAEP-3 Relay Expansion Board is shipped with a cable. It connects to the N-1000-III/ IV panel at J 1 . The next AEP-3 Expansion Board connects to the first as shown above. See the AEP-3 manual for further details.

## 6-6-4: PC to N-485-PCI-2

The $\mathrm{N}-485-\mathrm{PCl}-2$ uses a 9 pin female connector which will plug into a standard 9 pin serial port on the PC. If the PC is configured for use with a serial expansion device, an adaptor cable or shell may be required. Refer to the N-485-2 Adapter Installation Guide (TD1077) for proper configuration. Figure 6-11 shows a typical configuration with an $\mathrm{N}-485-\mathrm{PCl}-2$ at one end of the line and a panel at the other end.

## 6-6-5: N-485-PCI-2 to Panel

The NC2021/ NCP 2021 cable is recommended (twisted pair, 24 gauge overall shielded cable with a characteristic impedance of 120 ohms and a capacitance of $20 \mathrm{pf} / \mathrm{ft}$. (or lower) should be used). Consult the device installation guide for cable lengths and configurations.

The Communications Type Selection J umper J P1 must be set to the RS-485 position $2 \& 3$.

For 485 multi-drop communications, the shield should be connected at both ends and only to the screw terminals labeled " 485 Shield" as in figure 6-13. The shield should be tied to the earth ground only at the PCI or HUB.

For a typical configuration using the N-485-RLA Redundant Loop Adapter see figure 6-14.


Figure 6-11. Typical N-485 Multi-drop Configuration. This is a typical configuration with an $\mathrm{N}-485-\mathrm{PCl}-2$ at one end of the line and a panel at the other end. The AEP-3 is connected to the N-1000-III/IV at J 1 . See figure 6-10 for details of the AEP-3 connection.


Figure 6-12. Typical N-485 Multi-drop Configuration. This is a typical configuration using an $\mathrm{N}-485-H U B-2$ and modems and mixed N -1000 panels.

When mixing N -1000-II panels with the $\mathrm{N}-1000$-III/ IV in an N -485 dropline configuration, the N-485-API-2 panels must have the same version (1.xx, 2.xx...) as the N-1000-III/IV's "485 EPROM."

The dropline must be terminated at the ends. No other termination should be used at the panels which are not located at the ends.

For proper configuration of the N -1000-II in mixed loops, refer to the manuals for N-485-API-2, N-485-PCI-2, and N-485-HUB-2.

6-6-6: N-485-HUB-2 to Panel
The same cable should be used as the connection between the $\mathrm{N}-485-\mathrm{PCI}-2$ and the panels. The Communications Type Selection J umper J P1 must be set to the RS-485 position 2 \& 3. Figure 6-12 shows a typical configuration with a HUB using modems.

6-6-7: N-485-HUB-2 to Modem
The N-485-HUB-2 includes a DB-25 male connector, with a three foot long cable, to plug directly into the modem.


Figure 6-13. 485 Multi-drop Connection with Shielded Cable. A 485 communication loop with a PCl at the end of the line. NOTE: If the $\mathrm{N}-485-\mathrm{PCl}-2$ (or HUB-2) is wired between two panels and not at the end of the multi-drop communications wiring; it must be properly terminated. Terminate the last panel at one end only with JP 2 and JP3 inserted and JP4 off.


Figure 6-14. 485 Multi-drop Connection with N-485-RLA. The N-485-RLA Redundant Loop Adapter provides an altemate comunications route in the event that wiring is out or a short cirouit. Also, the signal is amplified at each node, allowing longer wiring distances. A minimum of three RLAs are required.

## 6-7: Cable Specifications

| Application | NCI Part \# AWG |  | Description | Max. Dist. | Imp. | Cap. |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| CPU to C-100-A1 | CBL-2 | 18 | twisted pr. shielded | 50' (15 m) | - |  |
| C-100-A1 to controller, controller to controller, controller to $\mathrm{C}-100-\mathrm{Al}$ | NC1821-GR | 18 | twisted pair overall shielded | 2000' (610 m) |  |  |
| N -485 connections or | $\begin{aligned} & \text { NC2021-GY-A } \\ & \text { NC2021-WH-A } \end{aligned}$ | $\begin{aligned} & 24 \\ & 24 \end{aligned}$ | twisted pair shielded 2 conductor | $4000^{\prime}(1200 \mathrm{~m})$ | 120 W | 20pf/t |
| N-1000-III/IV to AEP-3 |  |  |  | $5^{\prime}(1.1 \mathrm{~m})$ |  |  |
| CR-1, TR-1, CI-1, KR-1 Wiegand card readers | NC1861-BL | 18 | 6 conductors overall shielded | $500{ }^{\prime}$ (152 m) |  |  |
| NR-1 magstripe reader | NC1861-BL | 18 | 6 conductor shielded | 5001 (152 m) |  |  |
| PR-1-280 Cotag Reader: 280 read head to SZC SZC to $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$ | $\begin{aligned} & \text { NC1861-BL } \\ & \text { NC1861-BL } \end{aligned}$ | $\begin{aligned} & 20 \\ & 18 \end{aligned}$ | 6 conductor shielded <br> 6 conductor shielded | $\begin{aligned} & 300^{\prime}(91 \mathrm{~m}) \\ & 500^{\prime}(152 \mathrm{~m}) \end{aligned}$ |  |  |
| PR-2 Hughes Reader: scanner to reader reader to $\mathrm{N}-1000-111 / \mathrm{V}$ | NC1861-BL <br> NC1861-BL | $\begin{aligned} & 20 \\ & 18 \end{aligned}$ | 6 conductor shielded <br> 6 conductor shielded | $\begin{gathered} 30^{\prime}(9 \mathrm{~m}) \\ 500^{\prime}(152 \mathrm{~m}) \end{gathered}$ |  |  |
| PR-3, PR-5 Indala Readers $A-3 / A-5$ read head to RE-2 RE-2 to $\mathrm{N}-1000$ - III/V | NC18121-YL NC1861-BL | $\begin{aligned} & 24 \\ & 18 \end{aligned}$ | 12 cond. shielded <br> 6 conductor shielded | $\begin{gathered} 75^{\prime}(23 \mathrm{~m}) \\ 500^{\prime}(152 \mathrm{~m}) \end{gathered}$ |  |  |
| PR-20, PR-22 Indala Readers: A-20/A-22 read head to RE-2 RE-2 to $\mathrm{N}-1000$-III/IV | $\begin{aligned} & \text { NC18121-YL } \\ & \text { NC1861-BL } \end{aligned}$ | $\begin{aligned} & 24 \\ & 18 \end{aligned}$ | 12 conductor shielded 6 conductor shielded | $\begin{gathered} 75^{\prime}(23 \mathrm{~m}) \\ 500^{\prime}(152 \mathrm{~m}) \end{gathered}$ |  |  |
| PR-10, PR-12 Indala Readers: | NC1861-BL | 18 | 6 conductor shielded | 5001 (152 m) |  |  |
| HG-3 Hand Geometry Reader: | NC1861-BL | 18 | 6 conductor shielded | $500 '$ (152 m) |  |  |
| 11 Conductor Keypad | NC18121-YL | 18 | 12 conductor shielded | $500{ }^{\prime}(152 \mathrm{~m})$ |  |  |
| 5 Conductor Keypad | NC1861-BL | 18 | 6 conductor shielded | $500{ }^{\prime}(152 \mathrm{~m})$ |  |  |
| Alarm Input Points or | NC1821-GR NC 2221-BR | $\begin{aligned} & 18 \\ & 22 \end{aligned}$ | twisted pair, shielded | $2000{ }^{\prime}$ (610 m) |  |  |
| Relay Outputs or | NC 1821-GR <br> NC 2221-BR | $\begin{aligned} & 18 \\ & 22 \end{aligned}$ | twisted pair, shielded 2 conductor | $2000^{\prime}(610 \mathrm{~m})$ |  |  |

Note: for Plenum rated cable just add a "P" to Northern's part number (PN) prefix

## 6-8: NCI Cable Part Numbers

| Part Number | Description | Application | Length |
| :---: | :---: | :---: | :---: |
| NC1821-GR | 18 AWG/2 Cond. | Panel/Com. Cable | 1,000 |
| NC2021-GY-A | 24 AWG/2 Cond. | RS-485 Panel/Com. Cable | 1,000 |
| NC 1841-GY | 18 AWG/4 Cond. | Reader Cable | 1,000 |
| NC 1861-BL-500 | 18 AWG/6 Cond. | Reader Cable | 500' |
| NC 1861-BL | 18 AWG/6 Cond. | Reader Cable | 1,000 |
| NC 18121-YL-500 | 18 AWG/12 Cond. | Keypad Cable | 500' |
| NC 18121-YL | 18 AWG/12 Cond. | Keypad Cable | 1,000 |
| NCNET-1 | 50 Ohm | Network Cable | 1,000 |
| NC2221-BR | 22 AWG/2 Cond. | Alarm Cable | 1,000 |
| NC1821-OR | 18 AWG/2 Cond. | Power/DoorCable | 1,000 |
| NCC59206-BK | RG-59 | Video Cable | 1,000 |
| NC 242 P1LO-PR | 24 AWG/2 Twisted Pair, | PAK-TIME Clock | 1,000 |
| NCP1821-GR | 18 AWG/2 Cond. Plenum | Panel/Com. Cable | 1,000 |
| NC P2021-WH-A | 24 AWG/2 Cond. Plenum | RS-485 Com. Cable | 1,000 |
| NCP1841-GY | 18 AWG/4 Cond. Plenum | Reader Cable | 1,000 |
| NCP1861-BL-500 | 18 AWG/6 Cond. Plenum | Reader Cable | 500' |
| NCP1861-BL | 18AWG/6 Cond. Plenum | Reader Cable | 1,000 |
| NCP18121-YL | 18 AWG/12Cond. Plenum | Keypad Cable | 1,000 |
| NC P18121-YL-500 | 18 AWG/12 Cond. Plenum | Keypad Cable | 500' |
| NC PNET-1 | 50 Ohm Plenum | Network Cable | 1,000' |
| NCP2221-BR | 22 AWG/2 Cond. Plenum | Alarm Point Cable | 1,000 |
| NCP1821-OR | 18AWG/2 Cond. Plenum | Power/DoorCable | 1,000 |
| NCP59206-WH | RG-59 Plenum | Video Cable | 1,000 |
| NC P242P1LO-PR | 24 AWG/2 Twisted Pair Plenum | PAK-TME Clock Cable | 1,000 |

## 6-9: Config urations for RS-232 Serial Communic ation Ports

| PC's, DigiBoard or |  |  | WIN-EXP-8 or 16 connected to a MODEM or a C-100 (C setting) |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| PC/Digiboard DB 25 |  | DTE <br> male | WIN-EXP-8 or 16 |  |  | Modem DCE |  | female | C-100 (C) |  | female |
|  |  | DB | 25 | male | DB | 25 | DB |  | 25 |  |
| 1 | Shld |  | - | 1 | Shld | - | 1 | Shld | - | 1 | n/c |  |
| 2 | TxD | output | 2 | TxD | output | 2 | TxD | input | 2 | TxD | input |
| 3 | RxD | input | 3 | RxD | input | 3 | RxD | output | 3 | RxD | output |
| 4 | RTS | output | 4 | RTS | output | 4 | RTS | input | 4 | RTS | n/a |
| 5 | CTS | input | 5 | n/c |  | 5 | CTS | output | 5 | $\mathrm{n} / \mathrm{c}$ |  |
| 6 | DSR | input | 6 | DSR | to 20 | 6 | DSR | output | 6 | n/c |  |
| 7 | GND | common | 7 | GND | common | 7 | GND | common | 7 | GND | common |
| 8 | DCD | input | 8 | DCD | input | 8 | DCD | output | 8 | n/c |  |
| 20 | DTR | output | 20 | DTR | to 6 | 20 | DTR | input | 20 | n/c |  |
| 22 | RI | input | 22 | n/c |  | 22 | RI | output | 22 | n/c |  |


| PC's | or |  | WIN-EXP-8 or 16 connected to a $485-\mathrm{PCI}-2$ or EXP (SW1-1 \& SW2-1 closed <br> \& SW1-2 and SW2-2 open) |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| PC | DTE |  | WIN-EXP DTE |  |  | 485-PCI-2 |  | female | PC-EXP/CCTV |  |  |
| DB | 9 | male | DB | 9 | male | DB | 9 |  | DB | 9 | female |
| 1 | DCD | input | 1 | DCD | input | 1 | n/c |  | 1 | n/c |  |
| 2 | RxD | input | 2 | RxD | input | 2 | RxD | output | 2 | RxD | output |
| 3 | TxD | output | 3 | TxD | output | 3 | TxD | input | 3 | TxD | input |
| 4 | DTR | output | 4 | DTR | to 6 | 4 | DTR | to 6 | 4 | n/c |  |
| 5 | GND | common | 5 | GND | common | 5 | GND | common | 5 | GND | common |
| 6 | DSR | input | 6 | DSR | to 4 | 6 | DSR | to 4 | 6 | n/c |  |
| 7 | RTS | output | 7 | RTS | output | 7 | RTS | to 8 | 7 | n/c |  |
| 8 | CTS | input | 8 | n/c |  | 8 | CTS | to 7 | 8 | n/c |  |
| 9 | RI | input | 9 | n/c |  | 9 | n/c |  | 9 | n/c |  |


| C-100 (M setting) or |  |  | a 485-HUB-2 |  | connected to a MODEM |  |  |  | DB9/25 Adapter |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| C-100 (M) |  |  | 485 HUB |  |  | 485 MODEM DCE |  |  | DB9/25 ADAPTER |  |  |
| DB | 9 | male | DB | 9 | male | DB | 9 | female | DB9 |  | DB 25 |
| 1 | N/C |  | 1 | n/c |  | 1 | Shld | - | 1 | DCD | 8 |
| 2 | TxD | output | 2 | TxD | output | 2 | TxD | input | 2 | RxD | 3 |
| 3 | RxD | input | 3 | RxD | input | 3 | RxD | output | 3 | TxD | 2 |
| 4 | RTS | n/a | 4 | RTS | to 5 | 4 | RTS | input | 4 | DTR | 20 |
| 5 | CTS |  | 5 | CTS | to 4 | 5 | CTS | output | 5 | GND | 7 |
| 6 | DSR |  | 6 | DSR | to 20 | 6 | DSR | output | 6 | DSR | 6 |
| 7 | GND | common | 7 | GND | common | 7 | GND | common | 7 | RTS | 4 |
| 8 | n/c |  | 8 | n/c |  | 8 | DCD | output | 8 | CTS | 5 |
| 20 | n/c |  | 20 | DTR | to 6 | 20 | DTR | input | 9 | RI | 22 |
| 22 | n/c |  | 22 | n/c |  | 22 | RI | output |  |  |  |

General rules of thumb:

- Inputs are connected to outputs. Two outputs are never connected together.
- The hardware handshaking lines should be ignored in software (if possible).
- For connecting 2 DB25 DTEs together cross pins $2 \& 3,4 \& 5,8 \& 20$, and connect 6 to 20. (For connecting two DB9 DTEs together cross pins $2 \& 3,1 \& 4,7 \& 8$, and connect 4 to 6.)
- Keep cables away from electrical noise sources and less than 50 feet long.
- Use shielded cable and connect all of the units to the same earth ground point.


## Section 7: Locks and Suppression

An S-4 Suppressor Kit must be installed with every electrical switching device run through a relay contact on the N-1000-III/IV. The Suppressor Kit protects the N-1000-III/IV from the power generated by the collapsing magnetic field of an electrical load device.

The S-4 Suppressor Kit consists of two identical pieces. Each piece contains a resistor, a capacitor and tranzorb network. These parts are encapsulated in a rubber-like insulating material. One piece is installed across the relay contacts without regard to polarity. The second is installed within 18 inches ( 457 mm ) of the electrical load locking device, without regard to polarity. (See figure 7-1.)

DO NOT run the panel wires or the lock power wires in the same cable/ conduit as the reader/ communication line cables. The lock wiring should be at least 12 inches aw ay from the data lines, or it should have a separate conduit.

DO NOT power door locks with the same power supply used for the N-1000-III/IV or card readers.


Figure 7-1. S-4 Suppressor Kit Installation.

## Section 8: Grounding

It is important to ground all $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$ control panels properly. Each panel in the communication loop should be individually grounded.

In some locations, the individual earth ground is not adequate and a separate 1214 gauge Common Ground wire should be run along with the communication loop wire. Ground this wire at one location for the entire system. The 18 gauge or larger cable must be connected from the panel ground point directly to earth ground. The grounding point is shown in figure 8-1.

NOTE: The X-4 transformer's earth ground (TB9-1, see Section 4-7) may be used if an unsuitable earth ground exists. The transformer's 3rd ground prong must be plugged into the outlet.


Figure 8-1. Access Control System with Grounding Point. NOTE: The X-4 transformer's earth ground (TB9-1, see Section 4-7) may be used if an unsuitable earth ground exists. The 3rd ground prong must be plugged into the outlet. Use one or the other, DO NOT USE BOTH TB9-1 and the "local" chassis ground. The chassis earth ground is preferred.

Use one or the other, DO NOT USE BOTH TB9-1 and the "local" chassis ground. The chassis earth ground is preferred.

In installations where there is a significant amount of electrical noise, shields should be grounded outside of the panel.

The access control system should use a consolidated earth ground, in which the power line, telephone and access control system ground rods are bonded together. A consolidated earth ground eliminates the problem of step voltage blow out, in which measurable voltage potential exists betw een earth ground rods, resulting in a current flow path and damage to the access control system during a lightning strike.

Ground wire runs should be as short and straight as possible. Avoid sharp turns and use a minimum radius of eight inches ( 203 mm ) for bends. Ground wires should be run separate from other wires and be routed toward the earth. Do not run ground wires parallel to metal without proper bonding to the metal. Use of eight foot (2.4 $\mathrm{m})$ copper clad ground rods is recommended. Consolidated earth ground configuration is shown in Figure 8-2.


Figure 8-2 Access Control System with Consolidated Earth Ground.

## Section 9: Power

The N-1000-III/IV requires a $16.5 \mathrm{VAC}, 50 \mathrm{VA}, 60 \mathrm{HZ}$ or a 12 VDC linear ( 2 Amp continuous) primary power supply. If using an AC power source as primary power supply, connect the primary power supply to TB9 positions 4 and 5 , without regard to polarity. If using a DC power source as primary power, connect the primary power supply to TB9 positions 2 and 3, noting polarity. Use one or the other for primary power supply, not both. N-1000-III/IV AC power terminals are shown in figure 9-1.

Upon loss of primary power, the control panel is powered from the 12 VDC ( 4 amp/ hour) backup battery, if present, for up to 4 hours of full operation. Alarm point \#19 goes into alarm state upon primary power loss. Battery wires with faston connectors are soldered to the $\mathrm{N}-1000-\mathrm{IIII} / \mathrm{IV}$ panel. Use these to connect the 12 VDC battery to the panel, WITH regard to polarity. N-1000-III/IV (figure 9-1).

For backup function with 12 VDC power, select a 12 VDC power supply with battery backup capability and/ or use the standard 12 VDC backup battery provided


Figure 9-1 AC, Optional Extemal DC and Backup Power Connections.
with the control panel. If the external power supply has a relay contact output which indicates a power failure, then it should be wired to TB9-10 and TB9-11. J umper JP6 (Power Fail Signal) must be moved to Position 1 and 2 in order to enable the external input.

Upon loss of backup battery power, the control panel ceases operation, but retains database/ transaction buffer memory for up to 7 days.

Power-up Sequence for the N -1000-III/ IV:
Step 1: Set DIP-switch and verify jumper settings.
Step 2: Make primary power supply connections.
Step 3: Make backup battery connections.
Power-down Sequence for the $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$ :
Step 1: Remove backup battery connections.
Step 2: Remove primary power supply connections.

## APPENDICES

## AppendixA: Programming Quick Reference Guide

## Preface

The following Programming Guide applies to the $\mathrm{N}-1000-\mathrm{II} A$ and the $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$ panels. The text indicates where there are differences between the N-1000-IIA and the N-1000-III/ IV. These panels can be programmed using Northern's access control software installed on a PC. Information on Timezones, Areas, Panel variables and Access Levels can be managed automatically using this software.

These operations can also be performed via terminal or printer/programmer. This Programming Guide provides basic code format information necessary to program the $\mathrm{N}-1000$-IIA and $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$ control panels. For complete explanations and examples, contact the factory.

Note: If the $\mathrm{N}-1000-\mathrm{II} A$ or the $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$ is to be used in an existing $\mathrm{N}-1000-\mathrm{II}$ system, the N -1000-II must be at lease version 7.0 panel firmware.

## Notation Conventions

The following notation conventions are used:
bold Bold type represents literal elements of a command that must be entered exactly as shown.
italic Italic type represents variables that must be defined by the user. Descriptions of these variables follow this section.
_ The symbol shown represents a space
$<C R>$ The symbol shown represents the ENTER key (Carriage Return).
[ ] The symbols shown (brackets) surround optional command elements.
\{ \} The symbols shown (braces) surround two or more choices. A vertical bar separates individual choices within the braces: \{choice 1 | choice 2 | choice 3 \}. The user must select only one of the choices.

## Invalid Code Error Messages

| Error Message |  |
| :--- | :--- |
| AP | Violation |
| EX |  |
| NF | Code expired (Limited Use or Visitor status) <br> Code not found (programmed) in memory for the <br> selected card reader or keypad. |
| TR | Trace message (enabled with the "C" Sub-command <br> \#1/T option). Access is allowed. |

TZ
PN
SC

Timezone violation.
PIN number violation.
Site Code violation.

## User Defined Parameter Variables

| Parameter | User Response |
| :---: | :---: |
| code | card number or keycode (1-65535)* OR card num keycode range. In a card number or keycode range, specify the first and last codes in the range, separated hyphen (-). |
| codel | first code in a range (1-65535)* |
| code2 | last code in a range (1-65535)* |
| dev | device number, as shown below: |
|  | (N-1000-IIA) (N-1000-III/IV) |
|  | $1=$ card reader \#1 $1=$ card reader \#1 |
|  | 2 card reader \#2 $2=$ card reader \#2 |
|  | 3 card reader \#3 |
|  | 4 = card reader \#4 |
|  | 3= keypad \#1 5= keypad \#1 |
|  | $4=$ keypad \#2 $6=$ keypad \#2 |
| limit | maximum use limit |
| mm | month number (1-12) |
| $\operatorname{day}(\mathrm{s})$ | $\begin{aligned} & \text { day of week, as shown below: } \\ & 1=\text { Monday } \\ & 2=\text { Tuesday } \\ & 3=\text { Wednesday } \\ & 4=\text { Thursday } \\ & 5=\text { Friday } \\ & 6=\text { Saturday } \\ & 7=\text { Sunday } \\ & 0=\text { Holiday notation (all holidays) } \end{aligned}$ |
| dd | day of month (1-31) |
| deb | debounce time (1-255) in seconds |
| desc | descriptive name up to 12 characters |

* 1-9,999,999, 999 Initial with "OL" option 1-9,999,999,999,999,999 Initial with "Y" option

| Parameter | User Response |
| :---: | :---: |
| durA | duration of time as shown below: \{S_sec\| M_min | H_hr\} where $\mathrm{se}=$ seconds (1-63) $\min =$ minutes $(1-63)$ $h r=$ hours (1-63) |
| errors | error count (0-255) |
| fsn | format slot number (1-8) |
| format | format command parameters for a specific card reader and/ or cards (see F command) |
| group | group number (1-32) |
| h | numb mode ( $h=1$ to 65535 where: $1=15$ seconds, $2=30$ seconds, $3=45$ seconds, etc.) |
| h1 | start timezone: hours (00-23) |
| h2 | end timezone: hours (00-23) |
| hol | holiday number (1-32) |
| input | input number (1-16) are hardwire Panel/ System alarms  <br> (N-1000-IIA) (N-1000-III/IV) <br> 17 com failure 20 mA 17 com failure $485-20 \mathrm{~mA}$ <br> 18 aux failure 232 port 18 aux failure 232 port <br>  19 primary power <br>  20 tamper <br>  21 input ground fault alarm <br>  22 low power supply <br>  23 external 5 volt short <br>  99 panel restart |
| i/o | This symbol actually stands for \{I_\#,O_\#,G_\# \} and indicates that you must choose one Input, Output, or Group. Seeindividual parameter for details. |
| m1 | start timezone: minutes (00-59) |
| m2 | end timezone: minutes (00-59) |
| output | output number |
|  | ( $\mathrm{N}-1000-\mathrm{II}$ ) |
|  | N-1000 ( $1-4,13,14)$ |
|  | $\mathrm{N}-1000-\mathrm{X} \quad(1-8,13,14)$ |
|  | 1st AEP-3 (17-24) $13=r d r 1$ LED |
|  | 2nd AEP-3 (25-32) $14=r d r 2$ LED |


|  | Parameter User Response |
| :---: | :---: |
| output | output number (continued) |
|  | ( $\mathrm{N}-1000-\mathrm{II} / \mathrm{IV}$ ) |
|  | N-1000 ( $1-4,11,12,13,14) \quad 11=r d r 1$ LED |
|  | $\mathrm{N}-1000-\mathrm{X} \quad(1-8,11,12,13,14) \quad 12=r d r 2$ LED |
|  | 1st AEP-3 (17-24) $13=r d r 3$ LED |
|  | 2nd AEP-3 (25-32) $14=$ rdr 4 LED |
| pn | panel number (0-63); $\varnothing=$ global command |
| relock | component to relock as shown below: <br> \{।_input \| O_output | G_group\} See input, output, and group. |
| shunt time | shunt time as shown below: \{S_sec\| M_min | H_hr\} where $\mathrm{sec}=$ seconds (1-63) $\min =$ minutes $(1-63)$ $\mathrm{hr}=$ hours (1-63) |
| sss | card site code(s) (0-65535) |
| tz | timezone number (0-63) |
| tz link | timezone link number |
| \# delay count | 1-255 (255 = 6 seconds delay) |
| zgn | input group number 1-16 |
| des |  |
| C? | Command Structure Error |
| F? | Database Full Error |
| R? | Range Error |
| S? | Syntax Error |
| T? | Timeout Error |
| U? | Unable to Complete Error |

## Programming Order

The following command list serves as a general guideline for the chronological order of programming.

Command
I
I
T
D
F
N

L

H
G
P
V
0
C
M
E
A

W
Z

## General Purpose

Clear panel memory.
Initialize panel.
Set time.
Set date.
Set format commands.
Assign descriptive names.
Define timezones.
Set holidays.
Define groups.
Set interlocks.
Assign input/ output/ group information.
Assign input/ output/ group information.
Add codes (cards/ keycodes).
Set buffer/ print options.
Eliminates pin/ invalid attempt counting.
Assigns reader to pulse output/ input/ groups.
Defines alarm input type (NO, NC, \& supervision)
Creates groups of Inputs (zones).

## Commands

The following commands are listed in alphabetical order.

## A Command

## Sub-Command \#1

Function: Assigns reading devices (card readers/ keypads) to activate specified input points, output points and groups.
Syntax: _A=pn_dev_i/o_[D_dev]<CR>
Example: _A=1_2_I_5
A valid code use at card reader \#2 (device \#2) on panel \#1 shunts input \#5 for the duration of the input's set shunt time.

## Sub-Command \#2

Function: Eliminates required PIN use at specified card reader.
Syntax: _A=pn_dev_D_Ø<CR>
Example: _A=1_2_D_Ø
A PIN is not required for access at card reader \#2 (device \#2) on panel \#1.

## C Command

## Sub-Command \#1

Function: Adds single/ multiple codes to memory with special options.
Syntax: _C=pn_code_tz_dev[_i/o][_A][_K][_L_\#][_P_\#][_T][_N_desc]<CR> User options:

A: Set Anti-passback feature to "IN" (card added with in status)
K: No PIN required
L_\#: Limited code use
P: Personal Identification Number (PIN)
T: Trace code
N : Name (must not be used with access control software)
Example: _C=1_594_1_1_2_A
Card \#594 is valid during Timezone \#1 at both card readers \#1 and 2 (devices \#1-2) on panel \#1. The card can be used the first time at any reader (panel learns status). Then anti-passback rules apply. The odd numbered devices represent "In" readers; the even numbered devices represent "Out" readers.

## Sub-Command \#2

Function: Deletes single/ multiple code(s) from memory.
Syntax: _C=pn_code<CR> or _C=pn_code1-code2_Ø<CR>
Example: _C=1_25Ø-299_Ø
Codes \#250-299 are deleted from memory in panel \#1.

## Sub-Command \#3

Function: Generates code database reports based on user-selected parameters.
Syntax: _C=pn_\{L_limit|N|R|U|Z_tz\}[_code]<CR> User options:

L: Limited Use/ Visitor status specification
N: IN status
R: Full report
U: OUT status
Z: Timezone specification
Example: _C=1_L_3_1ØØØ-12Øø
The returned report includes only those codes from \#1000 to 1200 with three or fewer uses remaining, in panel \#1.

Sub-Command \#4
Function: Manually sets anti-passback IN/ OUT code status.
Syntax: _C=pn_\{I|O\}_card \# <CR >
User options:
I: Set IN status
O: Set OUT status
Example: "Reset" code
Cardholder \#750 enters the building in the morning via the "IN" reader (on panel \#1, device \#1) and exits without using the "OUT" reader ( panel \#1, device \#2) in the evening. The following morning, the cardholder is denied access at the "IN" reader (anti-passback violation).
The system operator enters the following command to set the status of card \#750 to OUT:_C=1_O_75ø
Card \#750 is then allowed entry at the "IN" reader.

## Sub-Command \#5

Function: Allows information in a card record to be modified without overwriting the whole command.
Syntax: _C=pn_T_card\#_tz_tz_dev <CR $>*$
_C=pn_C_card\#_DeviceList<CR>
User Options:
T: Reset Timezones
C: Deletes devices from the card without removing it from the panel
Example: _C=1_T_750_3_5_1
Card number 750 already exists in panel but is valid during Timezones 1 \& 6. Now it changes card 750 to be valid during Timezone 3 and 5 at device 1.
Example: _C=1_C_22939_3_4
Card number 22939 had access to devices 1-4 at panel 1. Card number 22939 now only has access at devices 1 and 2 at panel 1.
*If either tz is $\varnothing$ it will not overwrite the existing tz. If a device is listed it will be added to the existing device list.

## Sub-Command \#6

Function: Allow cards to have different timezones per reader or keypad on a single panel. Panel must be initialized with I Command, Z_Ø option.
Syntax:
N-1000-IIA _C=pn_card\#_tz1_tz2\{dev1/ dev3\}\{dev2/dev4\}<CR> or
N-1000-III/IV_C=pn_card\#_tz1_tz2_tz3_tz4\{dev1/dev5\}\{dev2/ dev6\}\{dev3\}\{dev4\}<CR>
Example: _C=1_750_1_3_1_2
Card number 750 is valid at panel during Timezone 1 on Reader 1 and Timezone 3 on Reader 2.

## Sub-Command \#7

Function: Immediately set the status of all cards to "OUT." Used with panels with Anti-passback.
Syntax: _C=pn_F<CR>
Example: _C=1_F<CR>
All cards in Panel 1 are set to "OUT" status.

## D Command

Function: Sets control panel date.
Syntax: _D=pn_mm/dd_day <CR>
Example: _D=Ø_2/6_3
The date is set for Wednesday, February 6 in all panels.
Leap Year
Syntax: _D=pn_2/29_Day of week 1-7
Note: The date command has the option of adding a four digit year after the day of the week.
Syntax: _D=pn_mm/dd_dow_[yyyy]<CR> If no year is specified, Feb. 29th will be skipped (as in non-leap years with the date specified.)

## ECommand

## Sub-Command \#1

Function: Establishes the timezone for required use of PIN at specified panel. (Note: Panel must also be initialized with the "P" option.

Syntax: _E=pn_P_tz <CR>
Example: _E=1_P_2
During Timezone \#2, a valid card/ PIN combination is required for access in panel \#1. Outside of Timezone \#2, only a valid card is required for access.

## Sub-Command \#2

Function: Pulses input point, output point or group, based on a selected number of consecutive invalid code uses.
Syntax:
_E=pn_C_errors_[R_\{device \# \}]\{I_input | O_output | G_group\}
<CR>
Example: _E=1_C_3_R_3_O_3<CR>
Upon three consecutive invalid code uses at device \#3 on panel \#1, output \#3 is pulsed.

## F Command

## Sub-Command \#1

Function: Sets the method of interpreting card read data for Wiegand type cards.
Syntax: _F=pn_fsn_format<CR>
User options:
CR-1 Wiegand Card SwipeReader: _F=pn_1_26_S_1_D_1_B1_B2_B3_B4
$\mathrm{Cl}-1$ Wiegand Card Insert Reader: _F=pn_1_26_I_S_1_D_1_B1_B2_B3_B4
NR-1 MagstripeSwipeReader: _F=pn_fsn_32_S_Ø_D_Ø_B1_B2_B3_B4
PR-1-280 Cotag Proximity Reader: _F=pn_fsn_32_S_Ø_D_Ø_B1_B2_B3_B4
PR-2 Hughes/IDI Proximity Reader: _F=pn_fsn_34_S_1_D_1_B1_B2_B3_B4
HG-1 Hand Geometry Reader: _F=pn_fsn_32_S_Ø_D_Ø_B1_B2_B3_B4
5-Conductor Keypad: $\quad$ _F $=$ pn_fsn_32_S_Ø_D_Ø_B1_B2_B3_B4
Dorado MagstripeCards: $\quad$ _F $=$ pn_fsn_34_S_1_D_Ø_B1_B2_B3_B4
Sielox Wiegand Cards: $\quad$ F $=$ pn_fsn_34_S_1_D_1_B1_B2_B3_B4
Sielox Proximity Cards: $\quad$ _F $=p n$ fsn_32_S_Ø_D_Ø_B1_B2_B3_B4
NCS 25-Bit Cards: _F=pn_fsn_25_S_1_D_1_B1_B4_B2_B3
NCS 29-Bit Cards: _F=pn_fsn_29_S_1_D_1_B1_B4_B2_B3
KiddeCards: $\quad$ F=pn_fsn_31_S_1_BØ_B2_B3_B4
Continental 36-Bit Cards: _F=pn_fsn_36_S_3_D_2_B1_B2_B3_B4
Continental 37-Bit Cards: _F=pn_fsn_37_S_3_D_2_B1_B2_B3_B4
Example: _F=1_2_32_S_O_D_Ø_B1_B2_B3_B4
The format command entered (as format \#2) enables use of Cotag Proximity Readers in panel \#1.

Subcommand \#2
Function: Sets the format for use with ABA encoded cards- 12 digits are used with PCPAK and WIN-PAK.
Syntax: _F=pn_M[_R]_positions 1-12<CR>

## User Option

R: Allows debugging of raw card number information.
Example: _F=1_M_Ø_Ø_Ø_Ø_Ø_Ø_1_2_3_4_5_6<CR>
The number indicates the digit number from the card. The position of the number in the format indicates where the digit will appear in the final mapped card number. A zero in the format will cause a zero to appear at that point in the final card read.
NOTE: User option R can be used as shown here when adding to existing information or with position specified as a separate command.

## G Command

Function: Defines output point groups.
Syntax: _G=pn_[Z_input]_group_outputs[_W_wgroup]<CR>
_G $=$ pn[Z_input]_group_outputs[_W_wgroup]<CR>
Example: _G=1_1_5_6_7_8
Group \#1 is defined as outputs \#5, 6, 7 and 8 in panel \#1.
User options:
W: Warning group
Z: Special output groups
Syntax: _G=pn_group_W_wgroup__Outputs<CR>
Example: _G=1_1_W_2_1_1_2<CR>
Creates group 1 on Panel 1 . Group 1 has relays 1 and 2 and uses group 2 as a warning group. (Group 2 must be created and given a pulse time.)
Syntax: _G=pn_Z_input_group_outputs<CR>
(When the group is used singly the input number functions only to signify that the group is Special and will follow the rules below. In the case where only one group is used this should be a " $\varnothing$ " in order to prevent action being transferred to the next group);
The single Special Group has one main function: It will not go on during its Timezone unless a card is swiped to turn it on. But it will go off automatically at the end of its timezone in the normal manner. This means, for example, if your front door is on a timezone and there is a snow storm preventing personnel from getting to the office, the door will not unlock.
Example: _G=pn_Z_Ø_2_3_4<CR>
Group 2 containing relays 3 and 4 is established. A timezone can then be attached to group 2 via the "U" command. When the timezone begins, nothing happens. When a card valid for group 2 is used, relays 3 and 4 go on. Subsequent reads during the timezone toggle relays 3 and 4 off or on. If they are on when the timezone ends, they go off.

## HCommand

Function: Sets holiday dates.
Syntax: _H=pn_hol_mm/dd<CR>
Deleting Holidays:
To delete an existing holiday database entry, enter the " H " command for the desired holiday number WITHOUT the month (mm) and day (dd) parameters.
Example: _H=Ø_1_12/25
Holiday \#1 is set as December 25 in all panels.

Function: Initializes the control panel, enabling features and programming options for future use and sets panel site code(s).
Syntax:
I=pn[_sss][_A][_B][_E][_F][_G][_K][_H_h][_I][_L][_M][_N][_OA][_OJ]
[_OL][_ON][_P][_R][_S][_U][_V][_Y][_Z_(̄][_Z_tzn]<CR>

User options:
A: Anti-passback
B: Continuous code reads (Busy)
E: Free egress
F: Forgiveness (with anti-passback)
G: Groups
K: Keypads
H: Card numb mode; h=1 to 65535 where $1=15$ seconds, $2=30$ seconds, $3=45$ seconds, etc.
I: IN/OUT command sent from panel
L: Limited Use Cards
M: Card Reader LED function reversed
N: Names
OA: Alarms are reported when coming out of timezone shunt
OJ : Format for 20 bit card numbers, the first 12 bits are interpreted as the site code and last 8 as the card number. The card number is sent to the head end software as a 12-digit number.
OL: Creates Wiegand card numbers by concantenating site code with card number. The result is transmitted as a 12 digit number.
-DO NOT add site codes to the panel with this option
ON: Normals are reported when coming out of timezone shunt
P: Personal Identification Numbers (PIN)
R: Restart
S: $\quad$ Supervisory Board (Not for use with N-1000-III/IV)
U: \# of cards panel should set itself for
V: Visitor Cards
Y: Long format ABA cards
Z_Ø: Splittimezone
Z_tzn: Indicates that all cards with timezone numbers higher than that number will use the next higher timezone at Reader 2. At Reader 1 they will use the timezone attached to the card number.

Global Specification
Control panels MUST be initialized on an individual basis. The global specification ( $\mathrm{pn}=\varnothing$ ) is NOT allowed with the "I" command.

## LCommand

Function: DefinesTimezones.
Syntax: _L=pn_tz_h1:m1-h2:m2_days[tz link]<CR>
Comment: Timezone definition consists of a reference number, start time, end time, day(s) of week specification and optional link number. The end period of a timezone goes until the last second of the last minute (e.g., 8:00-17:00-at 17:01 the door is locked).
A timezone definition alone causes no action. Timezones must be assigned to cards/ keycodes, input points or output points to cause action.
Example: _L=Ø_1_ØØ:ØØ-23:59_1_2_3_4_5_6_7_Ø
Desired Timezone: 24 hours every day, including holidays.

## M Command

## Sub-Command \#1

Function: Sets control panel buffer/ print options.
Syntax: _M = pn[_P][_D][_L][_V][_X][_F][_R]<CR>
User options:
P: the panel buffers all activity.
D: the panel does not buffer or print valid code activity
L: the panel buffers and prints all activity
V: the panel buffers only valid code activity and prints invalid codes and alarm input point activity.
X: the panel buffers all activity and prints invalid codes and alarm input point activity
F: the panel "dumps" the buffer when it is within 100 transactions of capacity
R: the buffer is dumped, all buffer/ transmit options are cleared
Example: _M=1_P
Panel \#1 buffers all activity and transmits no activity.

## Sub-Command \#2

Function: Sets control panel anti-passback and "print names" mode of operation.
Syntax: _M=pn_\{A|_N|_B|_E| _K| _G\}<CR>
User options:
A: Enable anti-passback
N: Disable anti-passback
B: Disable names
E: Print names

Example: _ $\mathrm{M}=1 \_\mathrm{N}$
Panel \#1 is operating in the anti-passback mode.
Result: panel \#1 is taken out of the anti-passback mode.
Sub-Command \#3
Function: Disables global "OK" message.
Syntax: _M=Ø_K <CR >

## Sub-Command \#4

Function: Time delay betw een transmissions.
Syntax: _M=pn_\# delay count<CR>
Default: delay count=1
M aximum $=255$ at 6 seconds between transactions.

## N Command

## Sub-Command \#1

Function: Assigns 12 character descriptive names to codes (cards and keycodes).
Syntax: _N=pn_C_code_desc < CR >
Example: _N=1_C8562_」ONES,_MIKE
Code \#8562 is assigned the name J ONES, MIKE in panel \#1.
Sub-Command \#2
Function: Assigns 12 character descriptive names to control panels, card readers and keypads.
Syntax: _N=pn_R_dev_desc < CR>
Example: _N=1_R_Ø_LOBBY_PANEL
Control panel \#1 is assigned the name "LOBBY PANEL"
Example: _N=1_R_6_BACK_KEYPAD
Keypad \#2 ( $\mathrm{N}-1000-\mathrm{IV}$ ) is assigned the name "BACK KEYPAD"

## Sub-Command \#3

Function: Assigns 12 character descriptive names to input points, output points and groups.
Syntax: _N=pn_i/o_desc <CR >
Example: _N=1_I_6_BACK DOOR CT
Assigns the name BACK DOOR CT to input 6 on panel 1.

## O Command

## Sub-Command \#1

Function: Provides manual control and sets special options for input points, output points and groups.
Syntax: _O=pn_i/o_\{A|C|D|E|I|P|O|T|Z\}<CR>
User options:
A: Acknowledge alarm
C: Clear all options
D: De-energize/ Unshunt
E: Energize/ Shunt
I: Interlock disable
P: Pulse/ Shunt (Definable Duration)
O: Keep on
T: Returns a point to its Timezone when removed by the 0 command with the D option.
Z: Timezone control disable
Example: Group \#3 (outputs \#1, 3, 5, 7) in panel \#60 is de-energized and assigned a 12 second pulse time.
Command: _O=6Ø_G_3_P
Result: Group \#3 (outputs \#1, 3, 5, 7) pulses for 12 seconds.

## Sub-Command \#2

Function: Used to control input point groups (Zones) which are created using the Z Command.
Syntax: _O=pn_Z_zgn_E<CR> or
_O=pn_Z_zgn_D<D>

## PCommand

Function: Sets interlocks between alarm input points and/ or output points.
Syntax: _P=pn_i/o1_i/o2_\{D|E|F|IN|P|S\}_\{D|E|F|I|N|P|S\}<CR>
User options:
D: de-energize
E: energize
F: follow
I: invert follow
N : no action
P: pulse
S: pulse off
Example: i/ o1: Input \#5 (motion detector)
i/ o2: Output \#3 (siren)

Action 1: Energize
Action 2: De-energize
Interlock command: _P=1_I_5_O_3_E_D
Result: When input \#5 goes into alarm state (motion detector triggered), output \#3 energizes (sounding the alarm). When input \#5 returns to normal state, output \#3 de-energizes (turning off the siren).

## RCommand

Function: Generates control panel database reports which are displayed on the system monitor and/ or printer.
Syntax: _ R=pn_\{C|G|H|I|O|o|P|p|T|V|Z\}<CR>
Report options:
C: Code (card/keycode) database
G: Groups
H: Holiday schedule
I: Initialization parameters
O: Output points
o: Short output points report
P: Input points
p: Short input points report
T: Timezones
V: Firmware version
Z: Z Groups

## TCommand

Function: Sets control panel time.
Syntax: _T=pn_hh:mm <CR>
Example: _T=Ø_17:45
The time is set to 5:45 P.M. in all panels.

## VCommand

Sub-Command \#1
Function: Assigns shunt times and timezones to input points WITH special options.
Syntax: _V=pn_I_input_shunt time_tz[_A_durA[_tzA]][_B[_tzB]][_C] [_D_deb][__K_rèlock][_S[_tzS]][_X][_N_name][_Y[_tzY]]<CR> User options:

A: Acknowledge required
B: Buffered mode activation
C: Clear options

D: Debounce
K: Enable auto-relock
S: Disable alarm/normal messages
X : Reverse alarm/normal states
N: Nameinput point
Y: Timezone control interlock
Example: "Alarm Acknowledge" message without timezone
Input \#5 (motion detector) monitors a secure area. Want the input to be shunted (de-activated) during Timezone \#20 for free access. Input shunt time is not applicable. Want "alarm acknow ledge" message activation.
Command: _V=1_I_5_Ø_2Ø_A_1 $\varnothing$
Result: Input \#5 is shunted during Timezone \#20 and is assigned a shunt time of 0 (not applicable). When input \#5 goes into alarm condition, an "alarm acknowledge" message is sent to the system monitor and or printer. The message is transmitted every 10 seconds until acknowledged with the following command: _O=1_I_5_A

## Sub-Command \#2

Function: Assigns pulse times and timezones to output points WITH special options.
Syntax: _V=pn_\{O_\}output_pulsetime_tz[_C][_L][_Y_tzY][_N_name]<CR> User options:

C: Clear options
L: Latch
N: Nameoutput point
Y: Timezone control interlock
Example: _V=1_O_4_8_Ø_N_FRONT_SIREN
Output point \#4 is not Timezone controlled and is assigned a pulse time of eight seconds. The output is assigned the name "FRONT SIREN."

## Sub-Command \#3

Function: Assigns pulse times and timezones to groups WITH special options.
Syntax: _V=pn_G_group_pulsetime_tz[_Y_tzY]_N_name<CR>
User options:
N : Name group
Y: Timezone control interlock
Example: _V=1_G_5_6_Ø_N_ALL_RELAYS
Group \#5 is not timezone controlled and is assigned a pulse time of six seconds. The group is assigned the name "ALL RELAYS."

W Command (Used with the N-1000-III/IV only)
Function: Program each Input point for either NO/ NC and Supervised or NONSupervised operation.
Syntax: _W=pn_input_\{SO|SC|NO|NC\}<CR>
SO: Supervised normally open (Resistor in parallel with switch);
SC: Supervised normally closed (Resistor in series with switch);
NO: Non-supervised normally open; and
NC: Non-supervised normally closed (default).
If an EOL Resistor is put on a point, and it is not set up as a supervised point (using one of the commands above), it will report TROUBLE whenever the resistance is connected. This should serve as a warning that the point has not been set up.
Example: _W=1_9_SO<CR>
Alarm Input \#9 has been programmed as supervised, normally open on panel 1.

## ZCommand

Function: Creates Groups of Inputs (Zones). Allows shunting of multiple inputs. One command can shunt all required points on the panel rather than one command for each point. Commands are similar to the " G " command.
Creation: $\quad Z=p n \_z g n \_i n p u t \_i n p u t$ etc.
zgn: $\quad$ The number of the group being created (up to 16 of these groups can be created)
input: The input number included in the group.
Control: Through "O" commands with energize and de-energize such as:

$$
\begin{aligned}
& \text { _O=pn_Z_zgn_E<CR> } \\
& \text { or } \\
& \text { _O=pn_Z_zgn_D<D> }
\end{aligned}
$$

## OR'ing OF INPUTS

If two or more inputs are interlocked to a single output to energize on alarm and de-energize on normal, both/ all inputs must be normal before the interlock will allow the relay to de-energize.

1. This "E" (Energize) action will allow situations where, for example, several input points can be interlocked to one relay which is driving a sounder, visual alert or digital dialer. When several inputs go into alarm and remain in alarm all at once, the relay will not be turned off after one of them goes back to normal. All Inputs must go back to normal before the output will be turned off.
2. The above operation is standard. An alternate method, where one input returning to normal would de-energize the relay, is offered as an option through the use of a terminal or text command. The command is written with a " G " designation in place of the "E" for energize.

Example: _ $P=p n_{1}$ I_input_O_on_G_D $<C R>$

## Output Groups by Readers

This feature allows a card to be used to activate one group when it is used at Reader 1 and a different group if it is used at Reader 2.

1. Initialization must include the " E " option and the " G " with a number following it. The number following the " $G$ " will tell the panel that Groups with a number higher than that number are to be used in the split method. If the number is EVEN, the jump described below will be 1. If the number is ODD, the jump will be 2. The ODD/EVEN distinction is used to distinguish the situation of regular groups, described here, from that of Paired Special Groups.
2. In setting up the groups and assigning them with access levels, it is necessary to keep in mind that group numbers higher than the number specified in the I command will be incremented by 1 when the card is used at Reader 2. (If the number above was EVEN. If the number above was ODD it will be incremented by 2.) The original group number will be used at Reader 1.
Example: _I=pn_E_G_4<CR>
_C=pn_444_1_1_2_G_3<CR>
_C=pn_555_1_1_2_G_7<CR>
Card 444 will pulse group 3 on either reader but card 555 will pulse Group 7 on Reader 1 and Group 8 on Reader 2.

## Appendix B: N-1000-III/V Compared to N-1000-II

## Miscella neous and Special Items

- Four layer PWB and other improvements provide greater electrical noise immunity.
- High efficiency switching regulators result in reduced heat generation and extended backup battery operation.
- The Four Reader Board on the N -1000-IV allows the panel to support a total of four readers. (See Section 4-3 for details.) This board mounts in the lower right quadrant of the board, and when it is present all 4 readers attach to it via its removable terminal strips. The regular reader terminals on the main board are not used when the Four Reader Board is used. The connector for this board is P3, located directly above the bottom center of the board. The Four Reader Board has its own run LED described in the LED section below. It is controlled by a special, programmable chip: called a PIC chip. The paper label on the PIC chip indicates a version number similar to the label on the PROM chips on the main board.
- A Reader Function Test is available on the $\mathrm{N}-1000-\mathrm{IV}$. A pair of jumper prongs located on the Four Reader Board and labeled "TEST" can be shorted to cause a function test. It will cause the board to generate a simulated card read (with the PIC firmware version \#) from each of the four readers.
- An improved canister has more knockouts, a larger battery bracket and improved wiring space, since the board is centered in the enclosure. (The old enclosure can be field retrofitted for the new board.)
- The processor speed of 9.8304 mHz is four times faster than standard $\mathrm{N}-1000-$ II. This increases the speed of internal processing. The communications output is the same as the $\mathrm{N}-1000-\mathrm{II}$ panels so they can be used in the same loop.
- An additional Terminal Block (TB9) has been added in the upper left quadrant of the board. This terminal block has an Earth Ground connection; a battery plus and minus (in addition to the soldered in battery wires.); AC connections 1 and 2 (there are no spade lugs for AC or battery); the Tamper and External Power Fail inputs with common terminals; and a place to tie the 485 shield if it is used.


## Memory

- One memory chip is soldered to the panel and a socket is provided for another. The X boards come with two chips.


## Communication

- There are both RS- 485 and RS-232 communications ports in addition to the 20 milliamp port. The 485 port is selected by a change of J umper 1 in lieu of the 20 milliamp. The RS-232 port can be used in addition to or in lieu of the other ports.
- The N-485-API-2 components are now on board the N-1000-III/IV panel eliminating the need for the separate API board. An isolated communications interface is used which is more immune to poor grounding. The terminals used are TB7 terminals $2(+)$ and $3(-)$. Back-up of the 485 memory is also provided by the "super-cap'.
- There is an onboard 9 pin RS-232 connector to allow a direct hookup to the AUX Port. The components for the AUX Port are on all N-1000-III/ IV boards, and the firmware is programmed to use it. This allows trouble shooting communication to an individual panel with a computer or terminal while the panel is still connected to the loop.


## Power

- The panel sits in standby mode if there is not enough voltage for reliable operation upon power up.
- The red primary power fail light flashes rapidly on reset or power-up if the battery is too low for reliable operation.
- A 16.5 volt AC power source is now required, allowing the charging of a 12 volt battery for back-up as well as the supply of 12 Volt Reader and PIR egress power from the $\mathrm{N}-1000-\mathrm{III} / \mathrm{IV}$.
- A 12 volt output 500 mA is available for powering 12 volt readers or PIR egress detectors. Connections for this are at TB9 terminals $7(+)$ and 8 (Common).
- Connections are provided for optional 12 volt DC at TB9 terminals 2 and 3. This is a change from the $\mathrm{N}-1000$-II where the 12 volt DC was connected to the AC terminals.
- Circuitry has been added to detect the drop-out and restoration voltage conditions when the panel is running from the power to the battery terminals. The processor is programmed to respond to both conditions. If the voltage on these terminals drops to its predefined level, the processor shuts the panel down. When the voltage rises to the predetermined level, the panel returns to operation. The voltage for drop out is higher (in the 10 Volt range) than it is for the N -1000-II (in the 9 Volt range).
- Battery Back Up Detector now detects switch-over on an external power supply (e.g., the PS1-12). In addition to detecting the switch-over from Primary Power to battery on the panel itself (as the N-1000-II always has) the new panel can sense if there is a switch over on an outside power source, such as the PS1-12, without the use of a relay on the outside power source.
- A special input is provided for wiring from power supplies that have a primary power fail signal. If this input point is used rather than the internal sensor described above, it must be selected by means of jumper J P-6. (1-2 = External and 2-3 = Internal. The default is for the internal sensor.)
- Detection of a primary power failure by either the internal sensor or the input from the relay on the external supply will generate an Alarm 19 not Alarm 8. Alarm Point 8 is now available for use as a standard alarm point
- Automatically resetting solid state fuses are used instead of replaceable glass fuses.
- The Green Low-V OK LED will go off to indicate low 12 Volt battery or disconnected RAM back-up.
- A special set of connectors for the AEP-3 Relay Expansion board is provided. J1 is a connector for the present AEP-3 Board design. J 2 is a double connector and will be used for a future development.


## Readers

- The Device Number is sent with the Card Read Transaction Message. The Card Read Transaction Message table is:

RD\#nn_ _ _ _ [Card Number] etc. $=$ Reader 1 or Keypad 1
RD\#nn__ X__[Card Number] etc. $=$ Reader 2 or Keypad 2
RD\#nn__ 3 _ _ [Card Number] etc. $=$ Reader 3
RD\#nn__ 4 _ _[Card Number] etc. $=$ Reader 4

- There are screw terminals for the reader cable shield connections. These are nearer to where the wires come in, eliminating the need to run the shield wire around the board.
- The default relationships betw een Readers, Outputs, Inputs and LEDs have changed. Readers 1 and 2 still activate Relays 1 and 2, but the default inputs for EGRESS are now 5 and 6 and the outputs for the Reader LEDs are 11 and 12 respectively. Keypads 1 and 2 function in the same manner. (As described below, the " $K$ " option is no longer needed in the initialization string because the terminals for the Keypads are not shared with inputs.) Readers 3 and 4 fire relays 3 and 4 and use outputs 13 and 14 for the Reader LEDs. The EGRESS inputs for these Relays will be 7 and 8.
- Split Timezones are allowed, one timezone per reader.
- The terminals on terminal block 8 are used exclusively for 11 wire Keypads. This makes all regular inputs available even when a keypad is used. The " $K$ " option is no longer needed. The " P " option must still be entered for use with PINs. The wiring is the same as it was on the $\mathrm{N}-1000-\mathrm{II}$ except that screw terminal 4 , which was skipped when wiring to the N -1000-II is used for the

Black (Ground) wire of the Keypad. (The Black wire was connected to screw terminal 12 on the N -1000-II.)

- Keypads 3 and 4 are no longer available. This means that when 4 readers are used, only two of them (Readers 1 and 2) can use PINs.


## Inputs

- There is a separate Tamper Input located on TB-9. Tamper Alarms report as Alarm number 20. This makes Input 12 (terminal now located on TB-6) available as a regular alarm input.
- As described above the default Input Interlocks have changed slightly. 1 and 2 are still the Door Contact Inputs for Doors 1 and 2, but now Inputs 3 and 4 are the Door Contact Inputs for Relays 3 and 4. Also the Egress Inputs for doors 1-4 are Inputs 5-8 respectively.
- Each Input can be individually programmed for both Normally Open or Normally Closed and Supervised or Non-Supervised operation.
There is a special command of the format:
W=pn_input_[SO,SC,NO,NC]<CR>
$\bar{W}$ here:
input = the number of the input point being set.
SO = Supervised N.O. (Resistor in parallel with switch);
SC = Supervised N.C. (Resistor in series with switch);
NO = Non-supervised N.O.; and
NC = Non-supervised N.C. (default).
- If an End Of Line Resistor is put on a point, and it is not set up as a supervised point (using one of the commands above), it will report TROUBLE whenever the resistance is connected. This should serve as a warning that the point has not been set up.
- When a point is programmed as supervised, a generic 2.2 K ohm $5 \%$ tolerance resistor is used as the End Of Line resistor.
- There are several changes to the System inputs.

Alarm 17 Communications failure alarm, but now it reports for either 20 mA or 485 failures.
Alarm 18 Reserved for future reporting of an Auxiliary Communications Failure (the RS-232 port.)
Alarm 19 Primary Power Fail alarm as described above.
Alarm 20 Tamper alarm (with special terminal connections on Terminal Block 9).
Alarm 21 Input Ground Fault alarm. If the input is shorted to Earth Ground this alarm will be generated. (Some fault conditions may generate an ALARM rather than TROUBLE from the point, but no ground fault will be interpreted as NORMAL.)
Alarm 22 Reserved for future use.
Alarm 23 Indicates an external 5 volt reader power short circuit.

Alarm 99 Generated at restart either due to the Push Button or the watch dog timer. (This alarm cannot be stored in the history buffer.) A 99 trouble is communicated on a cold boot-up reset when the RAM memory is being initialized.

- Terminals for Inputs 9 through 16 are now on terminal block 6 .
- Terminal block 6 terminals 1 through 4 are Inputs 5 through 8 (as they have been), but now terminals 5 through 12 are for Inputs 9 through 16. All 16 inputs are now available whether the 10/ 12 wire keypads are used or not.
- AEP-5 Supervised Input Expansion Board cannot be used with the N-1000-III/IV.


## Relays

- Relays are heavy duty inductive load rated. They have a maximum load rating of 30 VDC, 5 A Resistive, 2 A Inductive. They also have circuitry which is less susceptible to electrical switching noise.
- The ERB-1 Four Point Solid-State Relay Expansion Board can not be used with the $\mathrm{N}-1000$-III/ IV.


## Jumpers

There are several functions that are controlled by jumpers. (See Section 4-9.)

| Number | Position | Function |
| :---: | :---: | :---: |
| JP1 | Jump Pins 1 \& 2 | Selects 20 mA communications loop (default) |
|  | Jump Pins 2 \& 3 | Selects 485 Multi-drop Communications |
| JP2 | Open $\}$ | J umper inserted instead of JP4 if the |
| JP3 | Open | $\mathrm{N}-485-\mathrm{PCl}-2$ is not at the end of the multi-drop cabling. |
| JP4 | Jump Pins 1 \& 2 | Jumper is inserted for the panel at the end of the 485 multi-drop cable (default) |
|  | Remove | Remove the jumper if the panel is not at the end of the cable or JP2 and JP3 are used. |
| JP5 | (Not Used) |  |
| J P6 | Jump pins 2 \& 3 | Onboard power fail detection selected (default). |
|  | Jump Pins 1 \& 2 | Select External power fail detection. |
| JP7 | (Not Used) |  |
| J P8 | ( Not Used) |  |


| JP9 | Jump Pins 1\&2 <br> Remove | Disables clearing of RAM by JP10 (default). <br> Enables clearing the RAM by JP10. |
| :--- | :--- | :--- |
| JP10 | Jump Pins 1\&2 | Clears RAM when power is off and JP9 is re- <br> moved. |
| Remove | Will not clear RAM, allows supercap to backup <br> memory (default). |  |

JP11 (Not Used)

The LEDs have been rearranged and several new ones added. (See figure B-1.)
The functions of the $\mathrm{N}-1000$-III/ IV LEDs are listed below.

| Function LED |  | Color | Lit Indicates |
| :---: | :---: | :---: | :---: |
| Relay Status 1-8 |  | Red | Generally they are located near their respective relays and connections, except for LEDs 7 and 8, which are above and to the right of their relays. Lit indicates that its relay is energized, that is, the normally open relay contacts become closed and the normally closed relay contacts become open. Not lit indicates relay is de-energized, that is normally open and normally closed contacts are in normal state. |
| 485 Status | 9 | Green | Slow flashing indicates proper 485 communications. Stays lit indicates the panel address is not found or the baud rate is incorrect. Flickers rapidly and is somew hat dim, it is an indication that there has been a degradation in the bus signal. Rapid flashing (between blinking slowly and flickering rapidly) is an indication that there has been a loss of communication from the panel to the PCl or HUB. |
| +12 V OK | 10 | Green | The 12 volt power is available for supplying readers or PIR egress devices. |
| +5 V Ext. OK | 11 | Green | Panel is supplying + 5 VDC output for reader/ keypad power. |
| Low-V OK | 12 | Green | Both the Super Cap and the 12 volt gel cell battery are OK. |
| Ground Fault | 13 | Red | Continuity between earth ground and the panel's signal common. |
| Run | 14 | Green | Running Status indicator, functions as it always has. Pulsing indicates $\mathrm{N}-1000$ microprocessor operating properly; not lit or not pulsing indicates malfunction. |


| Power Fail | 15 RedLoss of primary power with panel powered by 12 <br> VDC battery (fast flashing indicates battery charge is <br> too low to operate panel even with primary power <br> available). Also flashes alternately with the RUN <br> LED when there is an incorrect PLD as described <br> above. |  |
| :--- | :--- | :--- | :--- |
| Com Loop | 16 YellowLit indicates the output terminals have a closed <br> circuit; flickering off indicates outgoing transmis- <br> sions are occurring. |  |
| 485-TX: | 17 Red $\quad$Periodic quick flashing indicates the panel is commu- <br> nicating on the 485 bus. |  |
| Four Reader | Goard Status | GreenQuick flash (strobe like) indicates processor running, <br> long flash indicates card read, very long flash indi- <br> cates reset due to error or power problem |



Figure B-1. Location of LEDs.

## Appendix C: Troubleshooting

Problem: The N-1000-III/IV control panel experiences "lock-ups."
Solution: Press the restart button to reset the panels and check the following:

- Verify a circular timezone link or interlock does not exist in the programming of the panel. See Appendix A or software operator's manual for programming details.
- Verify proper panel/ reader grounding procedures are followed. Improper grounding is a primary cause of panel lock-ups. See Section 8 for grounding guidelines. In environments where there is severe electrical noise, the shields should be grounded outside of the enclosure and a separate ground wire (12 gauge) should be used for the system ground, removing the individual panel grounds. Ground each panel to the single system ground wire and ground the single system ground wire at one point only.
- Confirm the installation of the two-piece S-4 Suppressor Kits with every electrical switching device run through a panel relay. See Section 7 for Suppressor Kit information.
- If all above attempts fail to correct the problem, completely power down the panel. See Section 9 for power-down instructions. After complete power-down, move jumper J P9 to Open and J P10 to Close. This will clear all RAM while the panel is powered-down. After a 60 seconds return JP9 to close and JP10 to open. This brings the $\mathrm{N}-1000$ back to its factory default settings.

Problem: Communication from the system programming device to the control panel(s) is not established ( 20 mA ).

Solution: When programming in a data terminal mode (Hyperterminal, Procomm, etc.) or printer programmer, press the SPACE BAR followed by the ENTER key to verify communication. If communication is established, each panel in the loop returns an S?. If communication is not established, check the following:

- Verify communication loop wiring. See Section 6 for panel and multipanel loop wiring specifications.
- A locked-up panel in a 20 mA configuration causes communication problems. Starting with the last (physical) panel in the loop and working back to the first ( physical) panel, press the restart button on all control panels in the loop. Check communication at the programming device after every restart to locate which panel is causing the lock-up. Check for proper grounding as mentioned above, and S-4 suppressor usage.
- Verify the baud rate of all control panels match that of the programming device. See Section 4 for baud rate DIP switch settings.
- Verify C-100 operation by shorting the red and white wires together, then shorting the black and green wires together (disconnecting these wires from the
loop). Enter into the terminal emulation program. Type some letters and press the ENTER key. The letters will echo back on the screen if the C-100 is working properly. See the C-100-A1 M anual for further details.

Problem: A card number is not transmitted to the programming device when a card is run through a reader.

Solution: Check the following:

- Verify communication from the $\mathrm{N}-1000$ to the programming device.
- Confirm the panel is not operating in the buffered mode.
- For readers requiring + 5 VDC power (from the control panel), verify voltage at the reader is +4.9 to +5 VDC.
- For readers requiring + 12 VDC power, verify output of external power supply.
- Verify card reader wiring. See Section 6 for general card reader wiring details.
- Wire the card readers directly to the $\mathrm{N}-1000$ (to eliminate reader to panel distance). If card reads are then transmitted, the problem is due to environmental factors.
- Determine whether the problem is related to the card reader in use or the N 1000 reader port in use. Wire the card reader in question to an $\mathrm{N}-1000$ reader port that is known to operate properly or wire a card reader known to operate properly to the reader port in question and test with known card.

Problem: A keycode is not transmitted to the programming device when entered at an Eleven-conductor Keypad.

Solution: Check the following:

- Verify communication from the N -1000 to the programming device.
- Confirm the panel is not operating in the buffered mode.
- Verify the keypad "\#" button is pressed after entering the keycode. Pressing "\#" button is necessary for keycode transmission.
- To test operation of an N -1000 keypad port without the use a keypad, simulate keypunches with a three-way jumper. To simulate a keypunch, connect one end of the jumper to a row terminal, one end to a column terminal and momentarily touch the third end to the keypad ground terminal. See Eleven-Conductor Keypad instructions (Section 6) for 2 of 7 matrix terminal information.

Example: Simulate pressing the " 4 " button.
Connect one end of the jumper to the brown wire terminal (column 1, TB8-1).
Connect the second end of the jumper to the purple wire terminal (row 2, TB8-6).

Momentarily touch the third end of the jumper to the keypad ground terminal (TB8-4). (See Section 6-3)

Simulate pressing the "\#" button.
Connect one end of the jumper to the green wire terminal (column 3, TB8-3).
Connect the second end of the jumper to the orange wire terminal (row 4, TB8-8).

Momentarily touch the third end of the jumper to the keypad ground terminal (TB8-4). (See Section 6-3.)

Individually simulate the pressing of each keypad button, as described above, to verify operation of the keypad port.

N1000-III/IV Self-Test Capability
The N-1000-III/IV firmware (version 08.01 .29 or later) has a built-in test capability that can be used to check the functioning of most of the circuit board's hardware. (The parts of the main circuit which are not checked with the built-in test mode include the entire RS-485 interface circuit- except the watch-dog timer).

Before conducting the self tests make sure that the Green "+5v EXT OK" and the "12 V OK" status LEDs are both ON. If present, disconnect the ribbon cable going to the Four Reader Board. Then perform the following DC voltage measurements:

| Test Point w/reference to Common | TestConditions | Nominal Value | Acceptable Range |
| :--- | :--- | :--- | :--- |
|  |  |  |  |
| Battery (+ ) TB9-2 to TB9-3 | Withoutbattery | 13.80 VDC | 13.70 to 13.90 VDC |
| 12 VDC output TB9-7 to TB9-8 | No Load | 13.80 VDC | 13.70 to 13.90 VDC |
| 5 VDC output TB5-4 to TB5-5 | No Load | 5.00 VDC | 4.95 to 5.05 VDC |

To enable the built-in test move all of the DIP switches to the ON position, and then reset the board. In the test mode the main micro's watchdog timer is disabled ( but not the 485 processor's watchdog ). The 485 com-port is not functional in the self-test mode so set jumper JP1 in the 20 mA position ( $1 \& 2$ ).

The circuit board will now function as follows:

1. Once after reset output Relays 1 through 8 will sequentially turn ON for a half second each. (Note: If no RAM is available then the built-in self test will stop after this step.)
2. The LOOP-ENABLE Relay (K9) will turn off for 4 seconds after reset and then a short stream of the ASCII "U" characters will be outputted followed by the message "Self Test Mode" on the RS-232 (P1) Port (set at 9600 baud 8 data, 1 stop bit and no parity). Characters inputted on either port will be echoed out the other port.
3. The RUN LED (and also the latch-IC U17 pin 16 output) follows the state of the Tamper Switch input TB9-9 (Closed = Low/ Off).
4. The POWER FAIL LED (and also the latch-IC U17 pin 15 output) follows the state of the microprocessor's Primary-Power-Fail input (with J P6 in 1\&2 and the Ext. P.-Fail input (TB9-11) closed or with JP6 in $2 \& 3=$ LED On).
5. The Reader \# 1 LED output (TB5-1) and the AEP-3 STROBE (J 1-1) line follows the state of the micro's Low-V input (LOW-V LED On = Reader LED output Low and J 1-1 High / open). The Low-V signal can be activated by moving jumper J P9 to location JP10 and then cycle the power source off and then back on. Restore JP9 to change the state. Shorting out the battery terminals (TB9-2 to TB9-3) will also generate a Low-V signal.
6. The Reader\#2 LED output (TB5-8) and the AEP-3 CLOCK (J 1-3) line follows the state of the micro's GFAULT input (GF-LED On = Reader LED output Low and J1-3 High / open).
7. The microprocessor's output port B follows the input port A as follows;
7.1. PBO (Input Mux-A line at U32-10) follows the state of input port PA0 (TB8-1).
7.2. PB1 (Input Mux-B line at U32-9) follows input port PA1 (TB8-2) (Closed = High).
7.3. PB2 (LED\#3 TB5-10) follows the state of input port PA2 (TB8-3). (Closed = Low).
7.4. PB3 (LED\#4 TB5-11) follows the state of input port PA4 (TB8-5). (Closed = Low).
7.5. PB4 (KeyPad\# 1 Select TB8-10) follows the state of input port PA5 (TB8-6).
7.6. PB5 (KeyPad\#2 Select TB8-11) follows the state of input port PA6 (TB8-7).
7.7. (PB6 is the Tamper Switch input and is not tested in this section.)
7.8. PB7 (AEP-3 Data J 1-2) follows the state of input port PA7 (TB8-8). (Closed= Low)
8. Relay Outputs (continuously after the initial reset sequencing):
8.1. With the TAM PER Switch input circuit OPEN (Tamper Switch not depressed) and the / Primary-Power-Fail input LOW (J P6 open) the relay outputs 1 to 8 will follow the settings of the S1 dip-switch positions 1 to 8 .
8.2. With the TAMPER Switch input circuit OPEN (Tamper Switch not depressed) and the / Primary-Power-Fail input HIGH (JP6 jumper 2\&3) the relay outputs 1 to 8 will follow the state of the Keypad Inputs in reverse order (TB8-1 = Relay \#8). When the Keypad input terminals are connected to common the respective Relays will turn ON with the exception that grounding P3-10 is required to turn ON Relay \#5. If the Four Reader Board is connected then Relay \#5 will oscillate on and off.
8.3. With the TAMPER Switch input circuit CLOSED (Tamper Switch depressed) and the / Primary-Power-Fail input HIGH (JP6 jumper 2\&3) the relay outputs 1 to 8 will follow the state of the Reader Data inputs and RAM memory test as follows:
8.3.1. Relay 1 pulses when Reader \#2, Data-0 (TB5-6) is tied to common.
8.3.2. Relay 2 energizes when Reader \#2, Data-1 (TB5-7) is tied to common.
8.3.3. Relay 3 pulses when Reader \#1, Data-0 (TB5-3) is tied to common.
8.3.4. Relay 4 energizes when Reader \#1, Data-1 (TB5-2) is tied to common.
8.3.5. Relays 5 and 6 both ON indicate that memory chip U6 is OK.
8.3.6. Relays 7 and 8 both ON indicate that memory chip U7 is OK.
8.4. With the TAM PER Switch input circuit CLOSED (Tamper Switch depressed) and the / Primary-Power-Fail input LOW (JP6 open) the relay outputs 1 to 8 will follow the state of the supervised inputs as follows; ( Note: EOL = 2.2K ohms)

## Resulting Relay States

| Input/TB | Input Condition | R1 | R2 | R3 | R4 | R5 | R6 | R7 | R8 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| With TB8-1 \& TB8-2 both OPEN: |  |  |  |  |  |  |  |  |  |
| Input \#1 TB7-4 | Open |  |  |  |  |  |  | Off | On |
|  | EOL |  |  |  |  |  |  | Off | Off |
|  | Closed |  |  |  |  |  |  | On | Off |
| Input\#5 | Open |  |  |  |  | Off | On |  |  |
| TB6-1 | EOL |  |  |  |  |  | Off | Off |  |
|  | Closed |  |  |  |  |  | On | Off |  |
| Input\#9 | Open |  |  |  | Off | On |  |  |  |
| TB6-5 | EOL |  |  |  | Off | Off |  |  |  |
|  | Closed |  |  |  | On | Off |  |  |  |
| Input\#13 | Open |  | Off | On |  |  |  |  |  |
| TB6-9 | EOL |  | Off | Off |  |  |  |  |  |
|  | Closed |  | On | Off |  |  |  |  |  |

With TB8-1 to Common \& TB8-2 OPEN:


Resulting Relay States (continued)
Input/IB Input Condition $\quad$ R1 $\quad$ R2 $\quad$ R3 $\quad$ R4 R5 R6 $\quad$ R7 $\quad$ R8
With TB8-1 OPEN \& TB8-2 to Common:

Input\#3 Open
TB7-6 EOL
Closed
Input\#7 Open
TB6-3 EOL
Closed
Input\#11 Open
TB6-7 EOL
Closed
Input\#15 Open
TB6-11 EOL
Closed

Off On
Off Off
On Off
Off On Off Off
On Off
Off On
Off Off
On Off
Off On
Off Off
On Off

With TB8-1 \& TB8-2 both to Common:
Input\#4 Open
TB7-7 EOL
Closed
Input\#8 Open
TB6-4 EOL
Closed
Input\#12 Open
TB6-8 EOL
Closed
Input\#16 Open
TB6-12 EOL
Closed
Off On

Off Off
On Off
Off On
Off Off
On Off
Off On
Off Off
On Off
Off On
Off Off
On Off

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